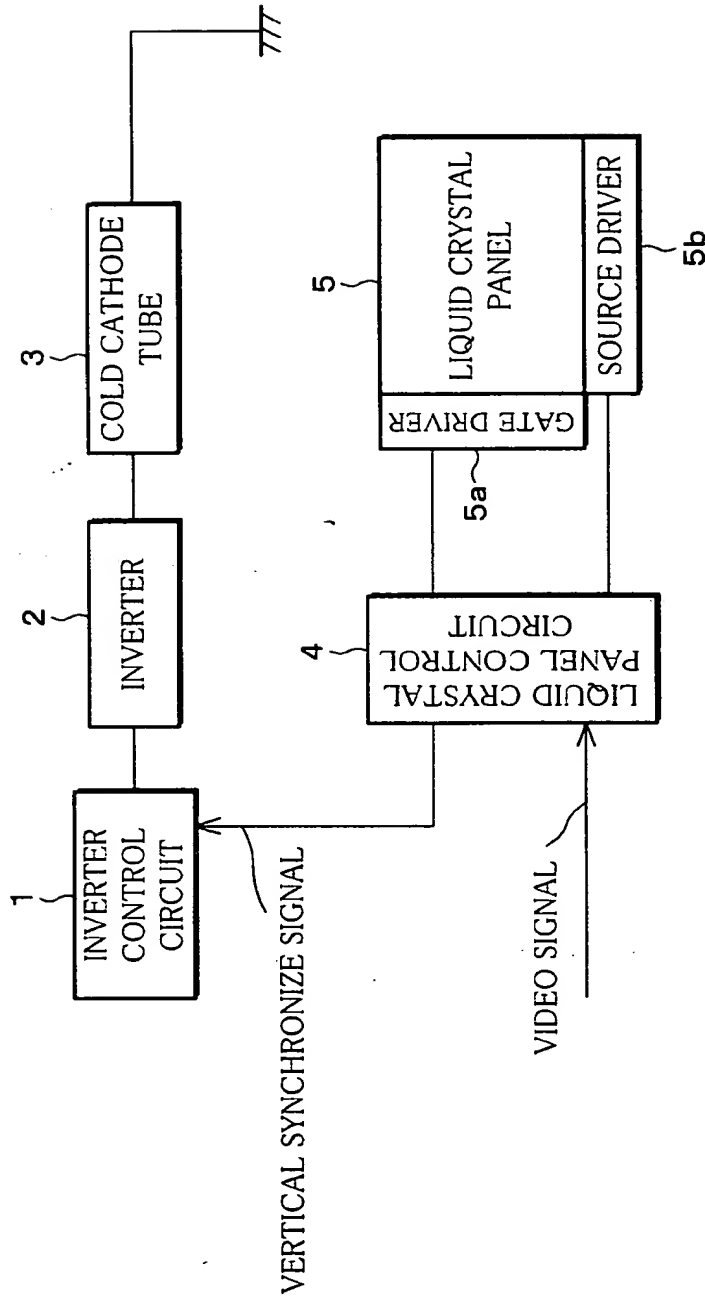
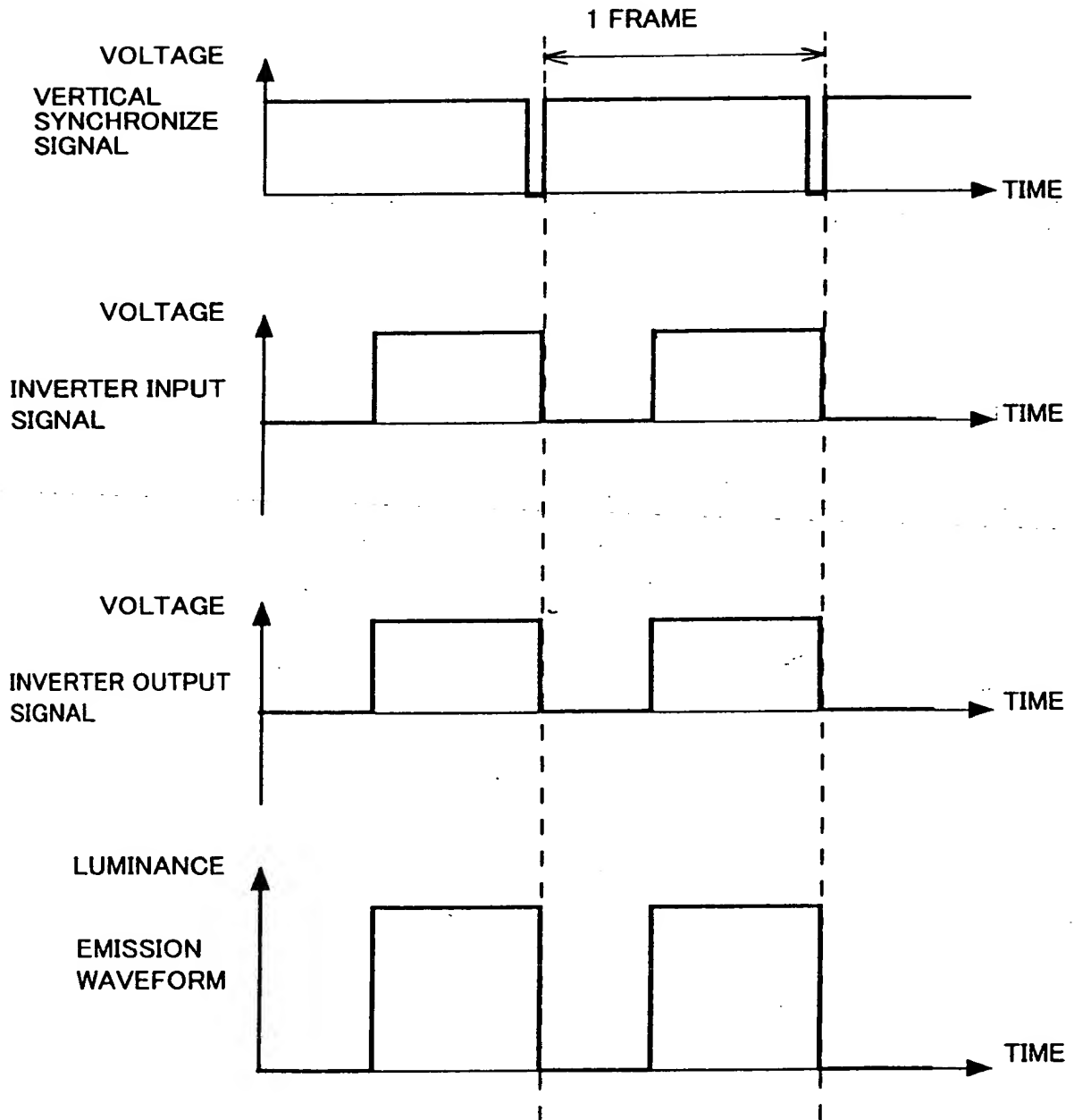


FIG. 1



F I G. 2



F I G. 3

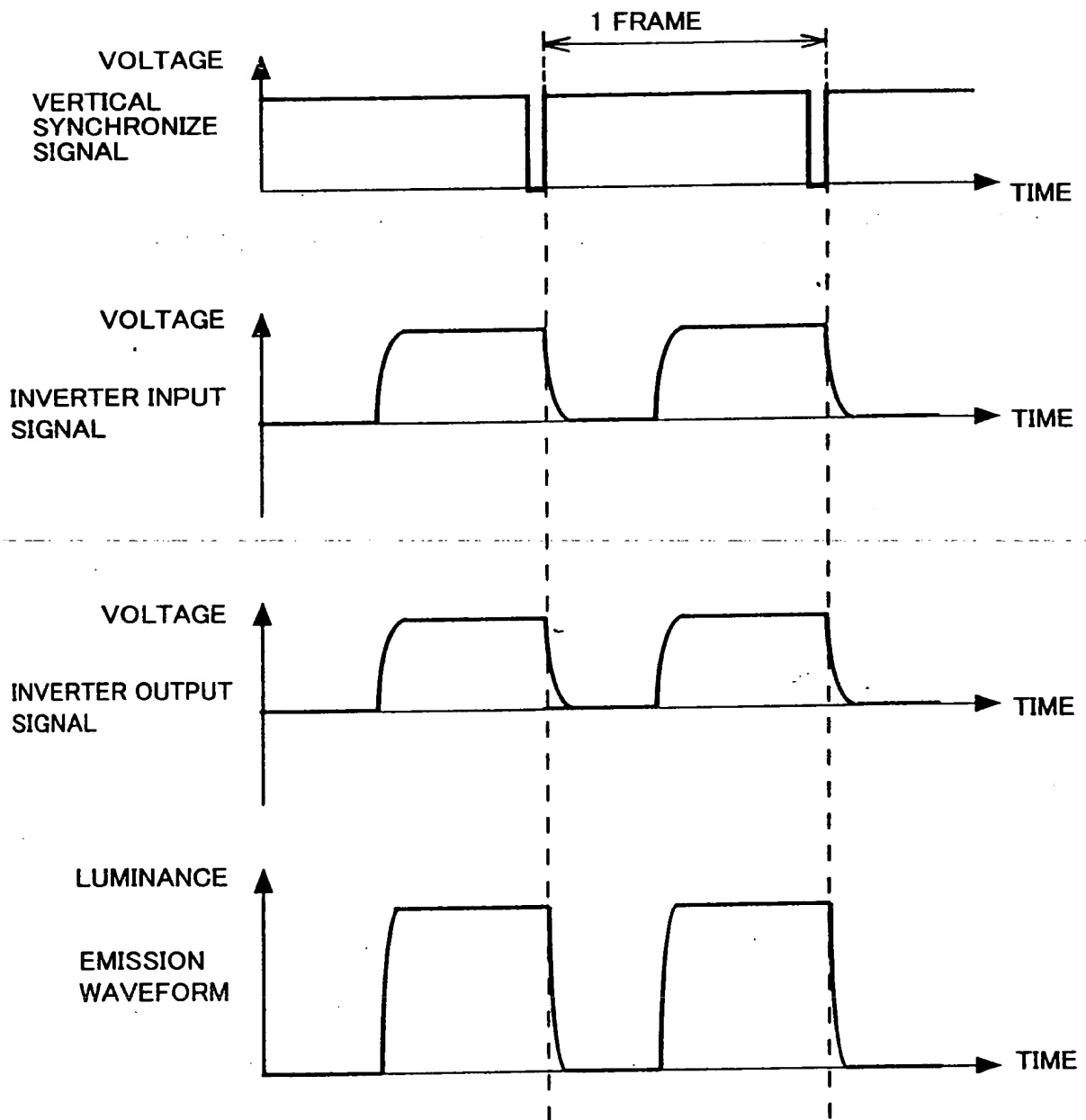


FIG. 4

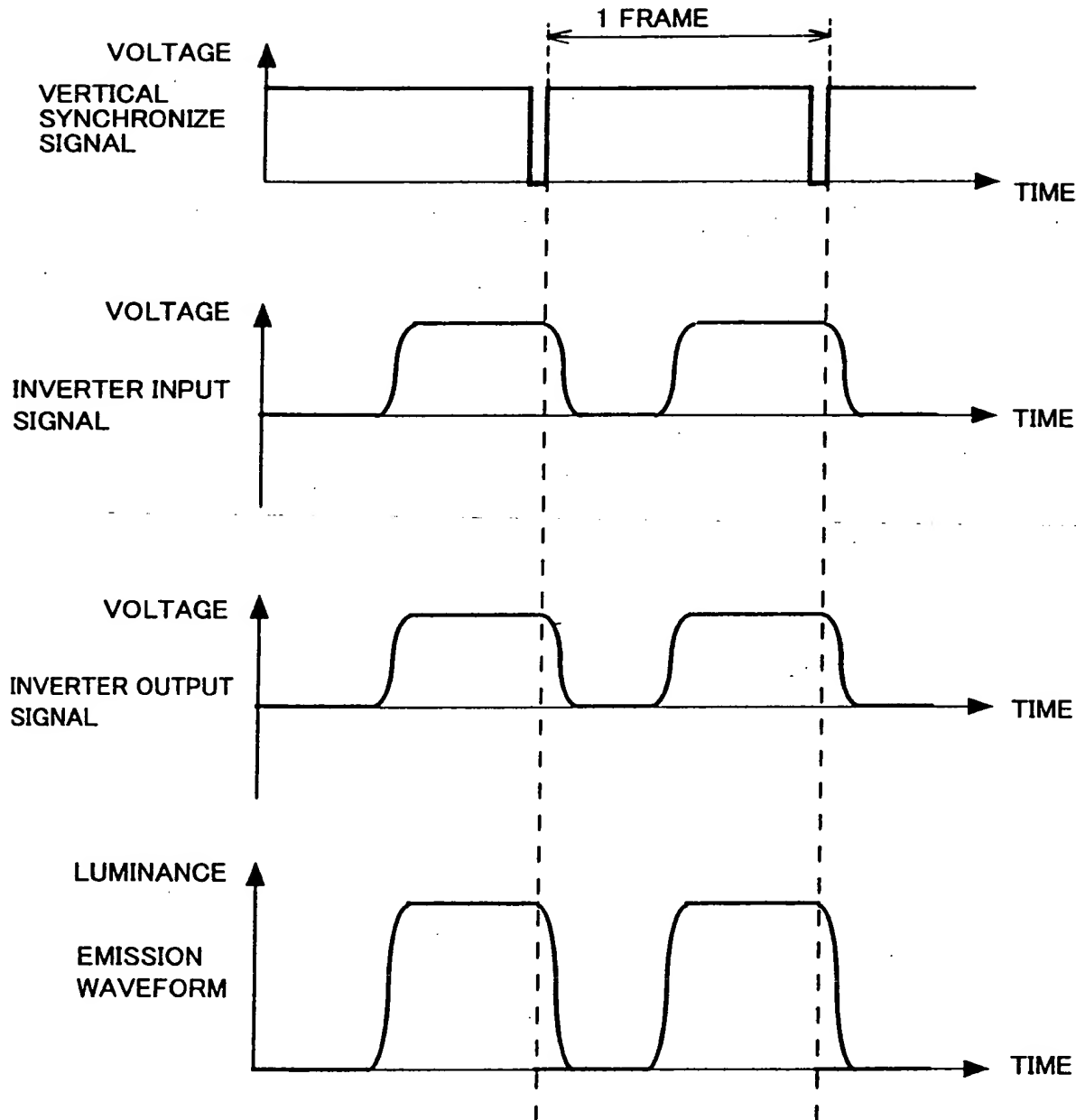


FIG. 5

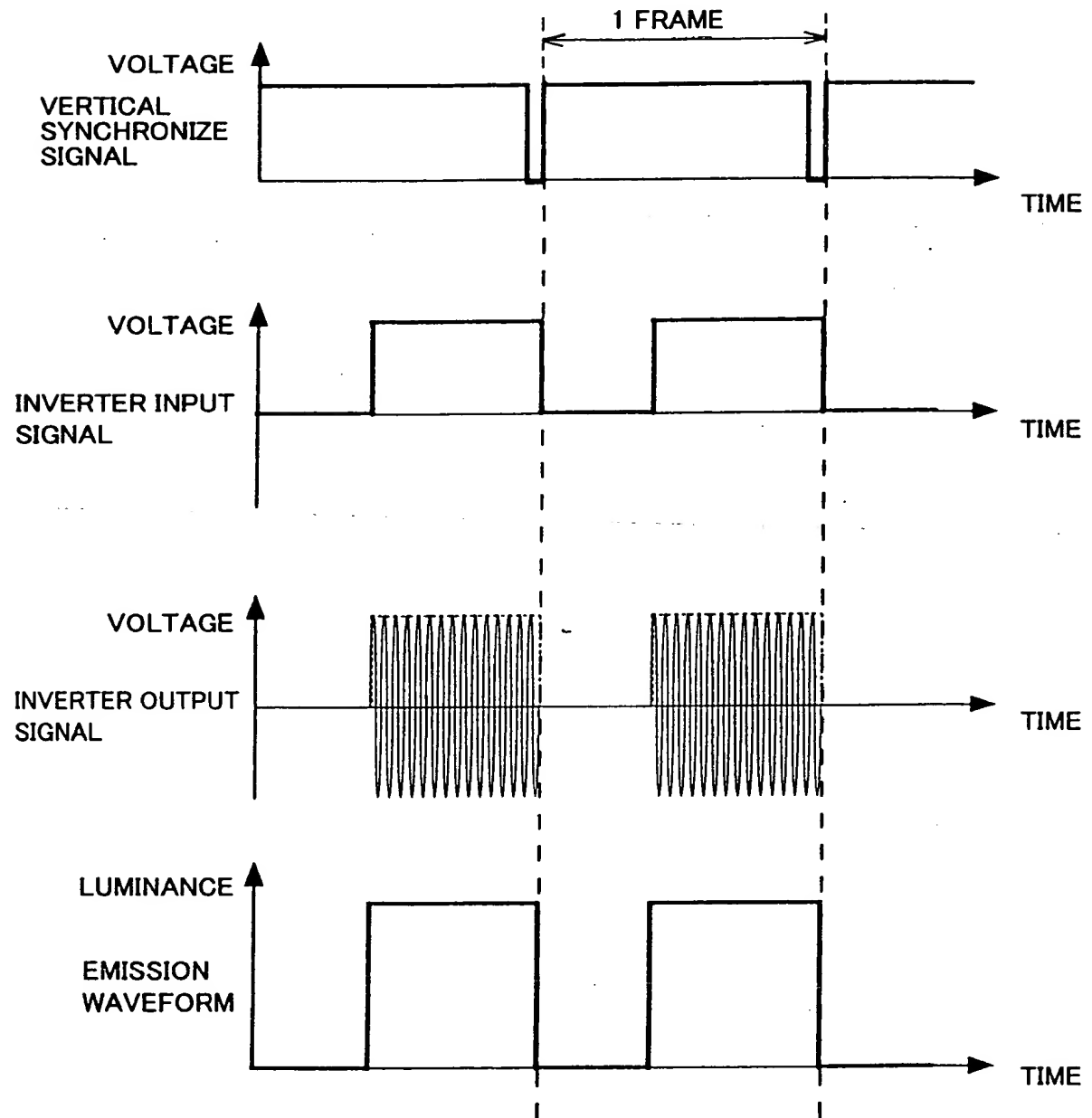
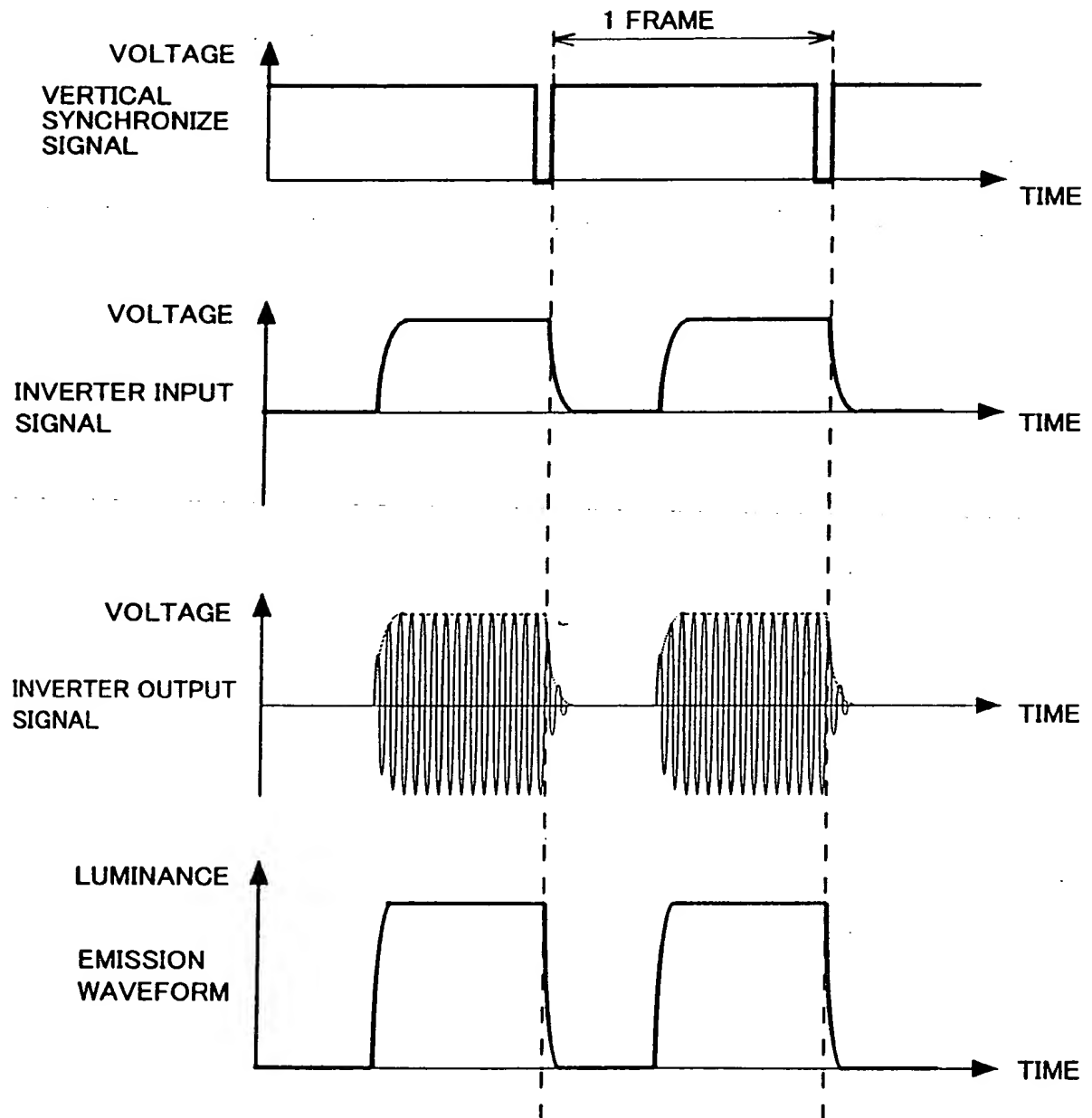
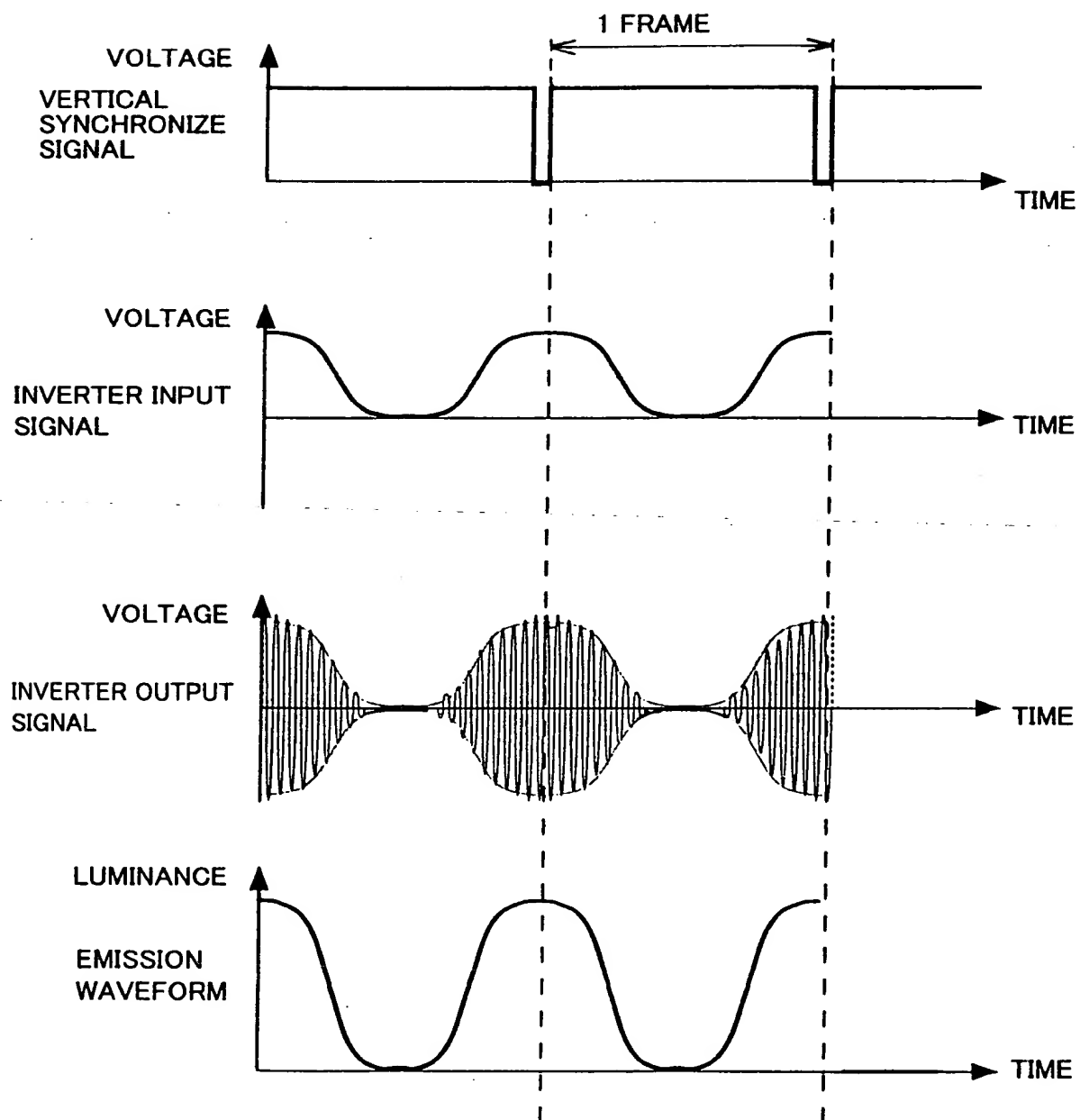


FIG. 6



[illegible]

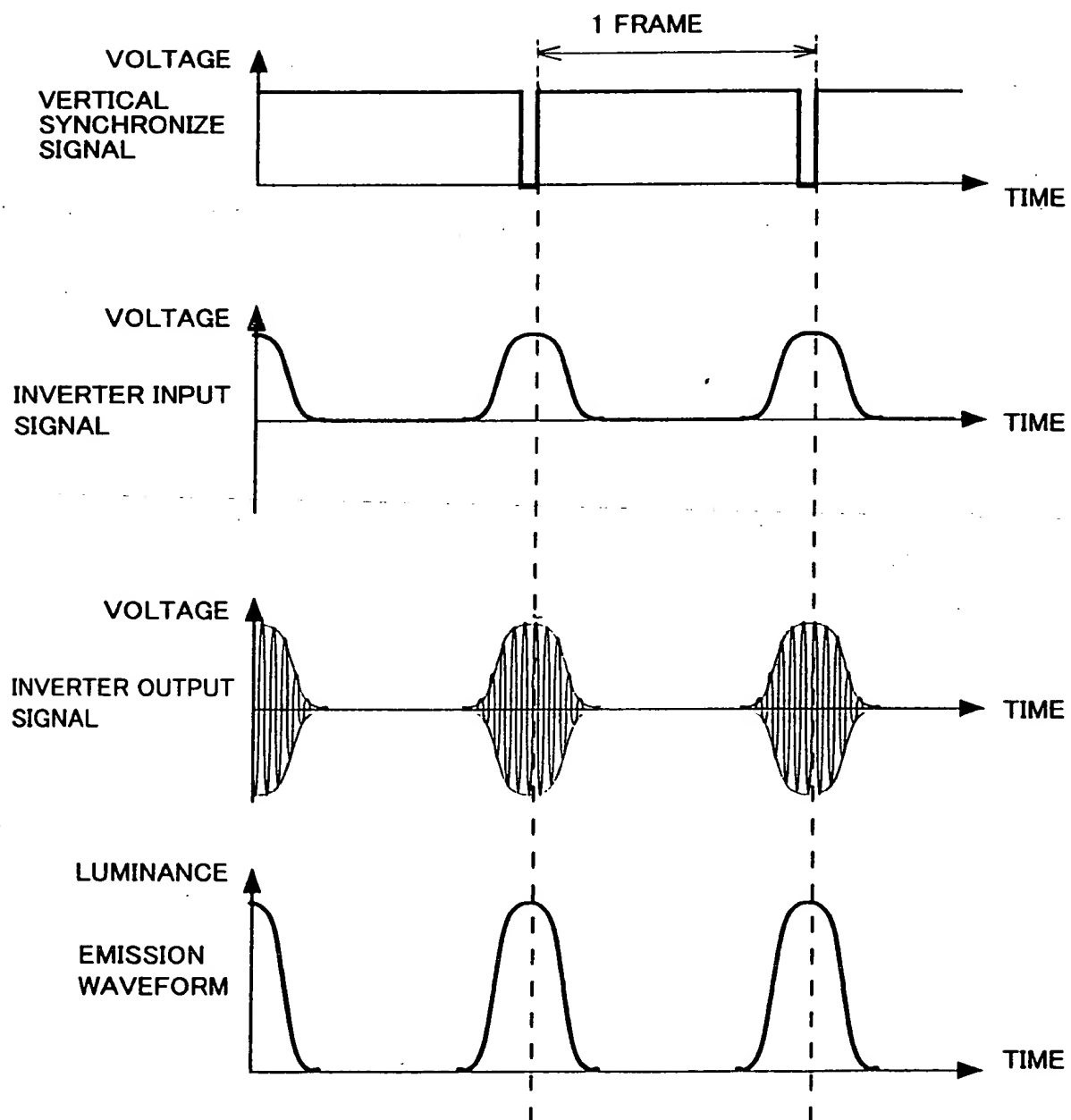
[illegible]

FIG. 9

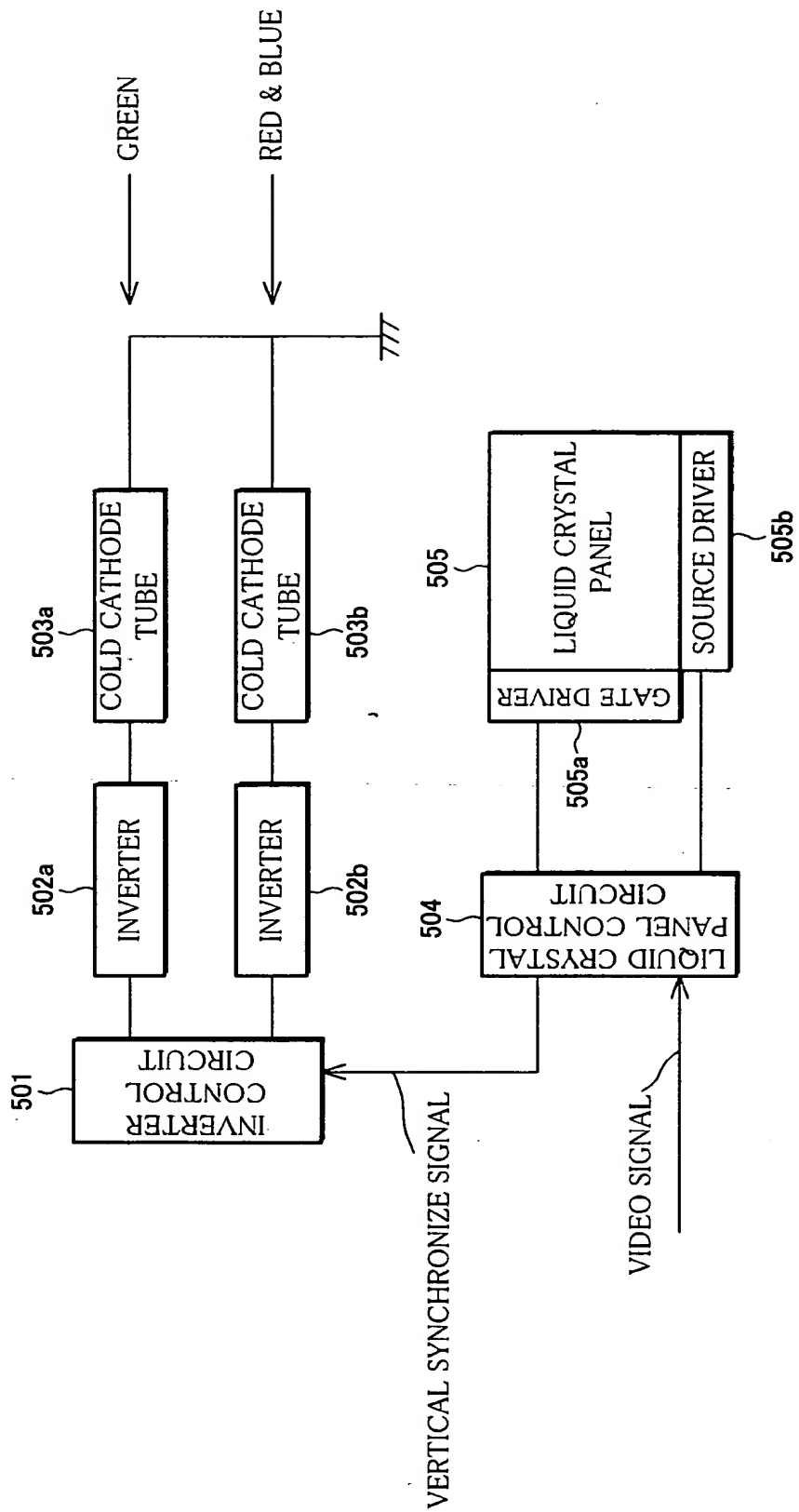


FIG. 10

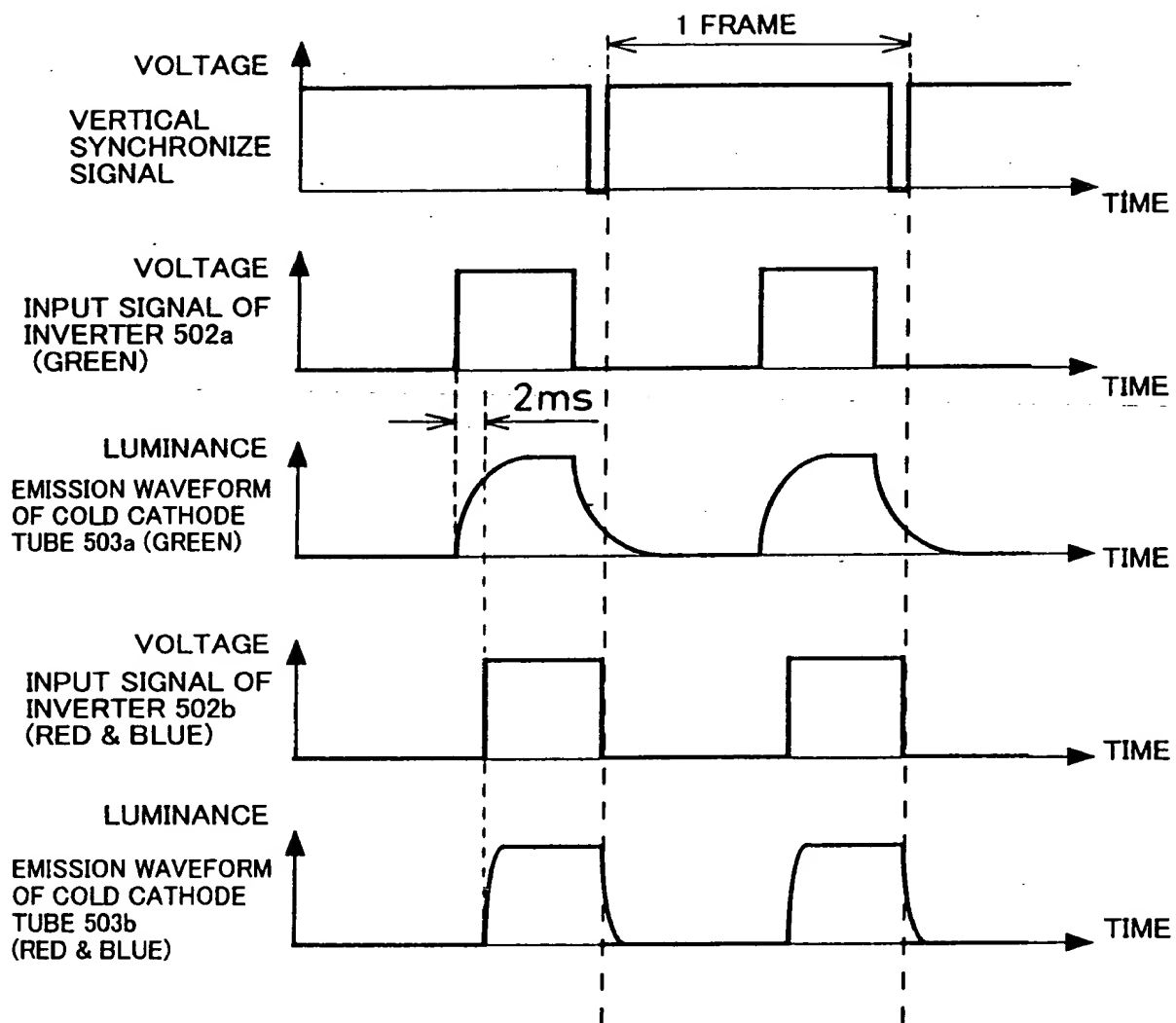


FIG. 10

FIG. 11

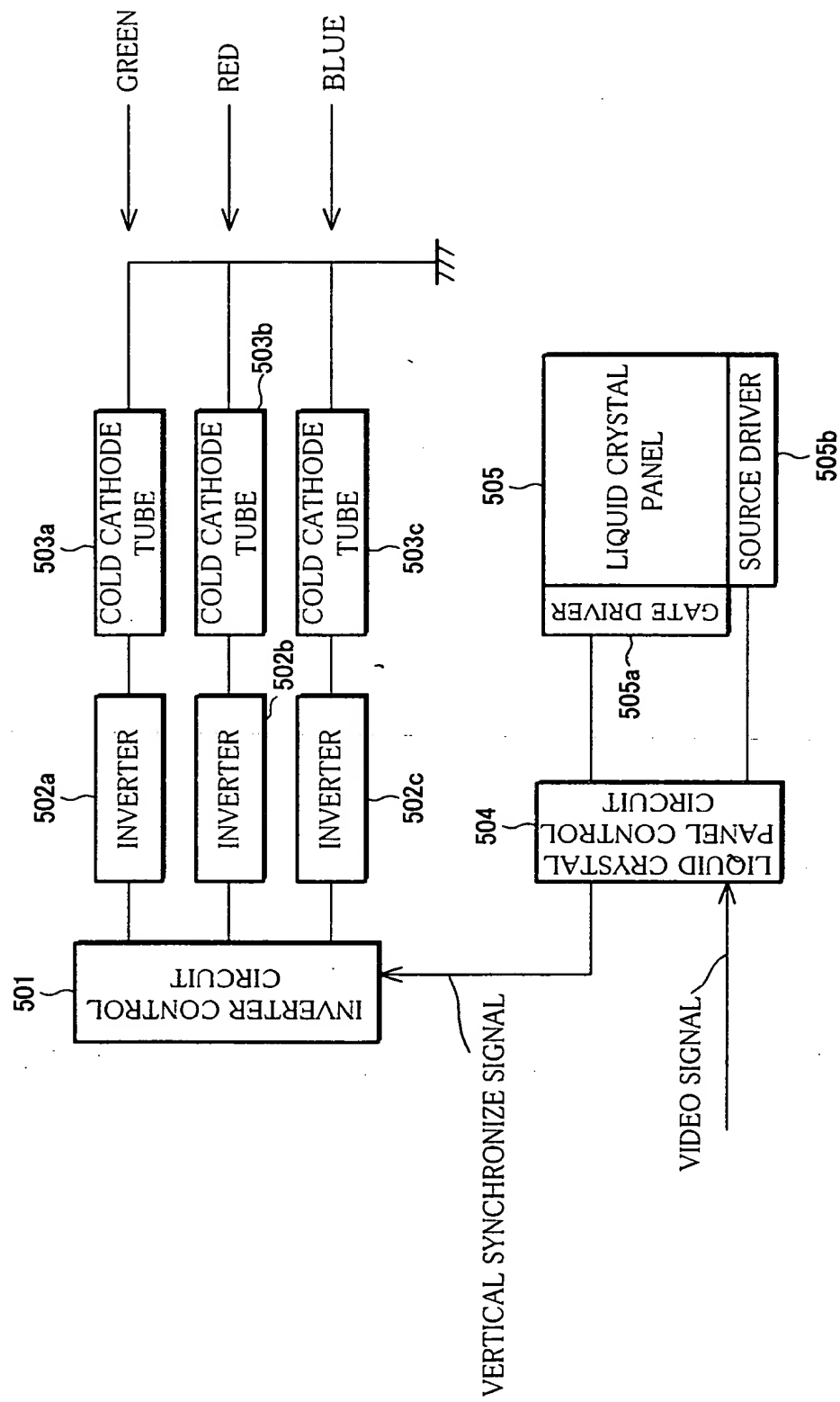


FIG. 12

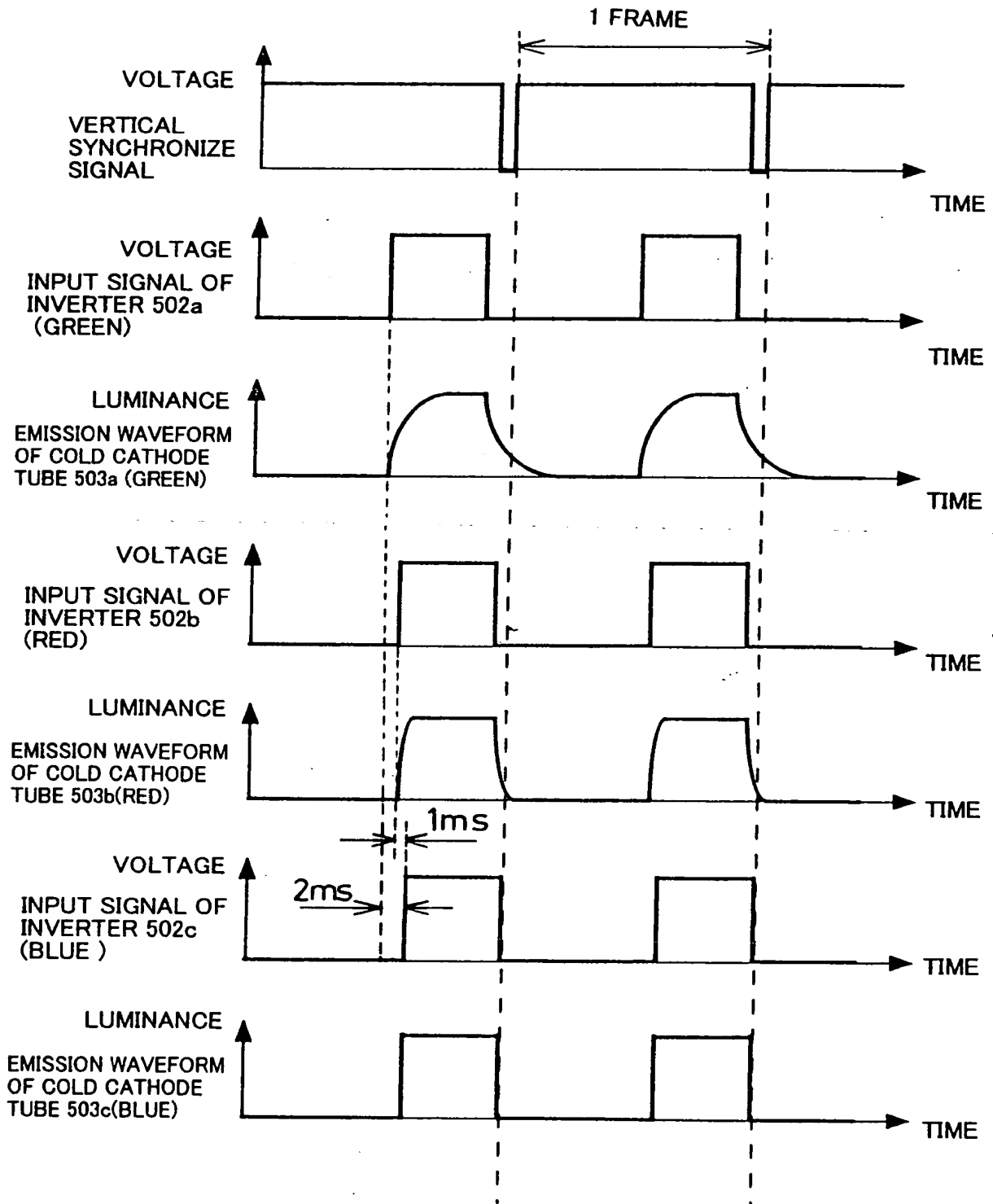


FIG. 13

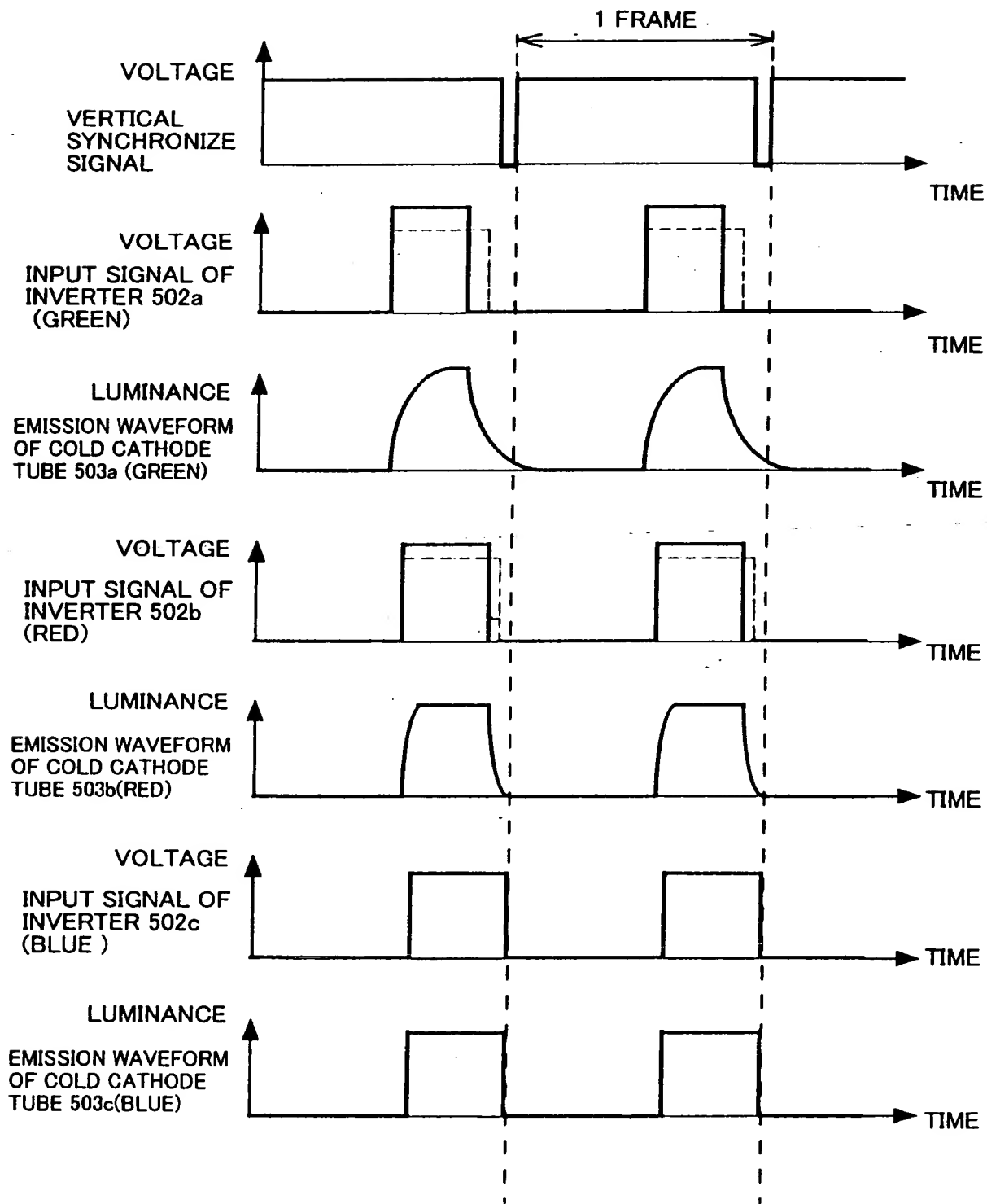


FIG. 14

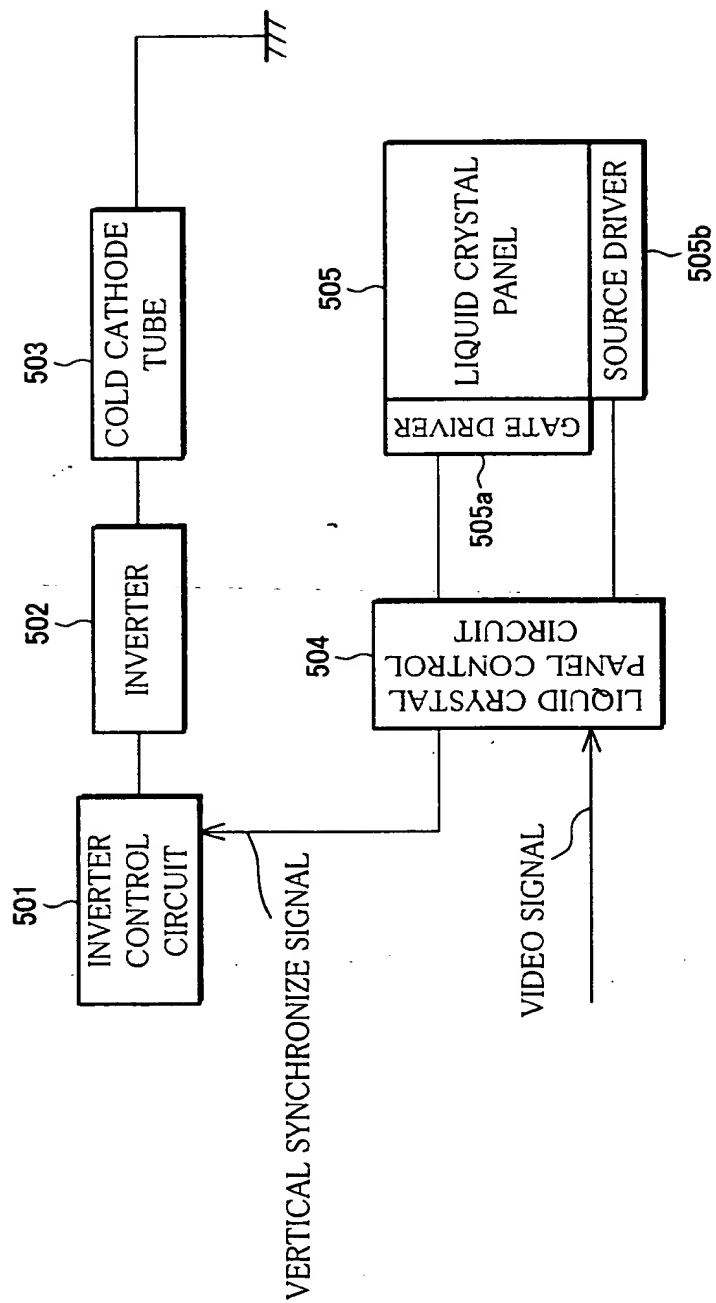
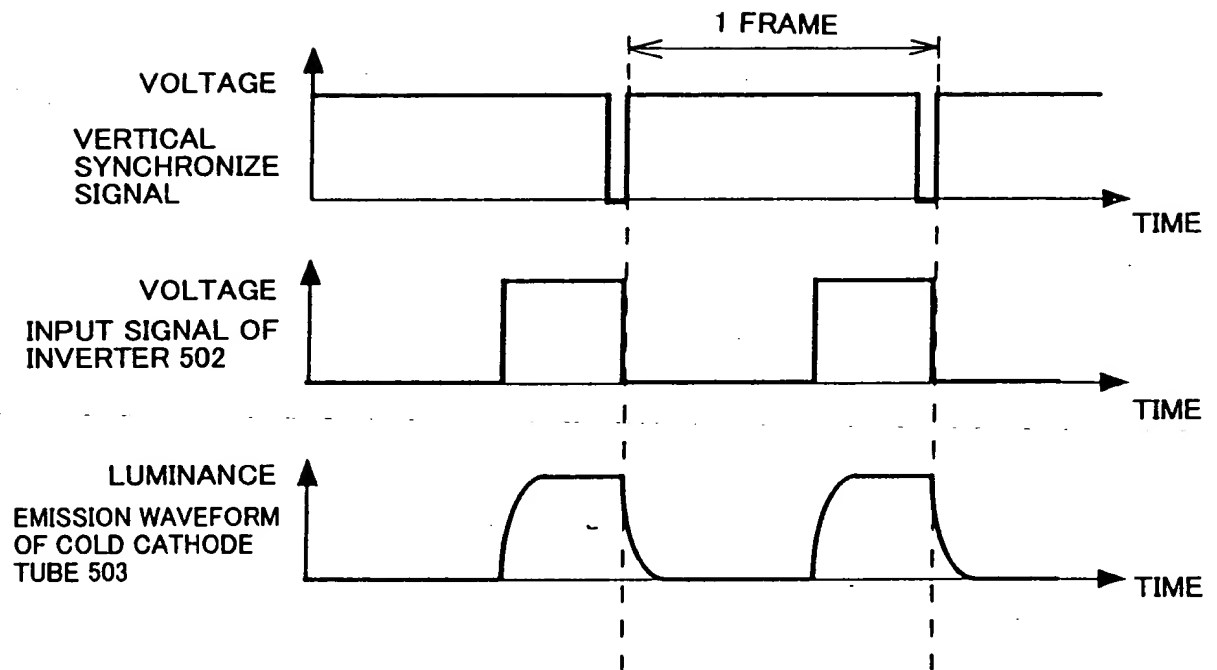


FIG. 15



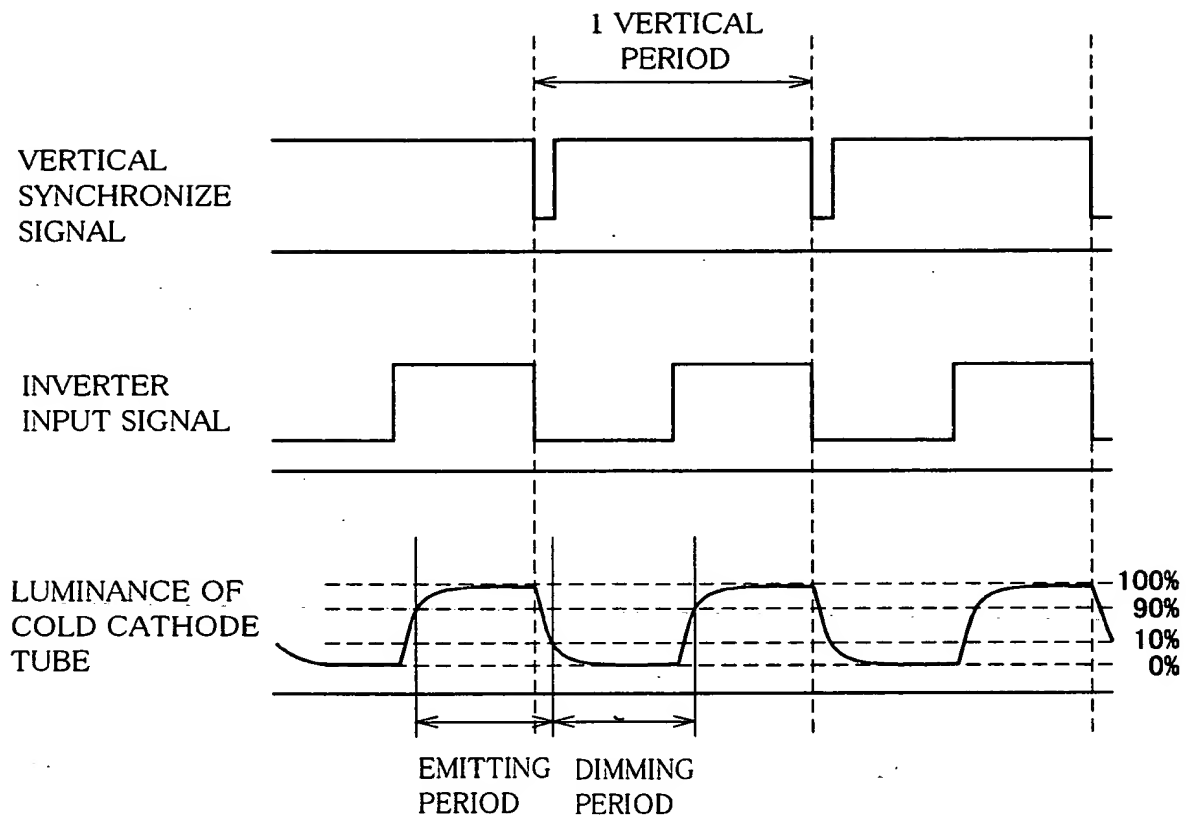
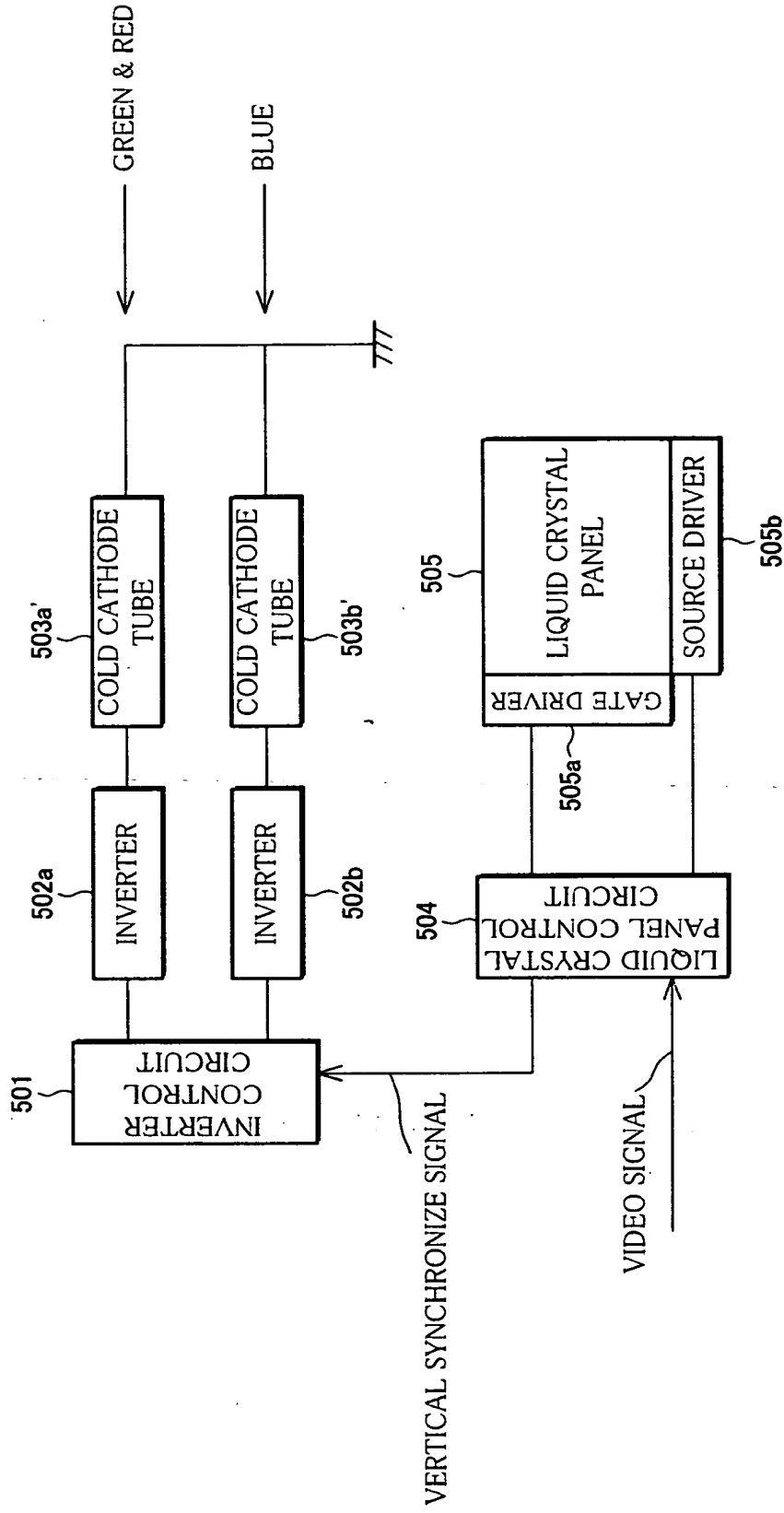
[illegible]

FIG. 17



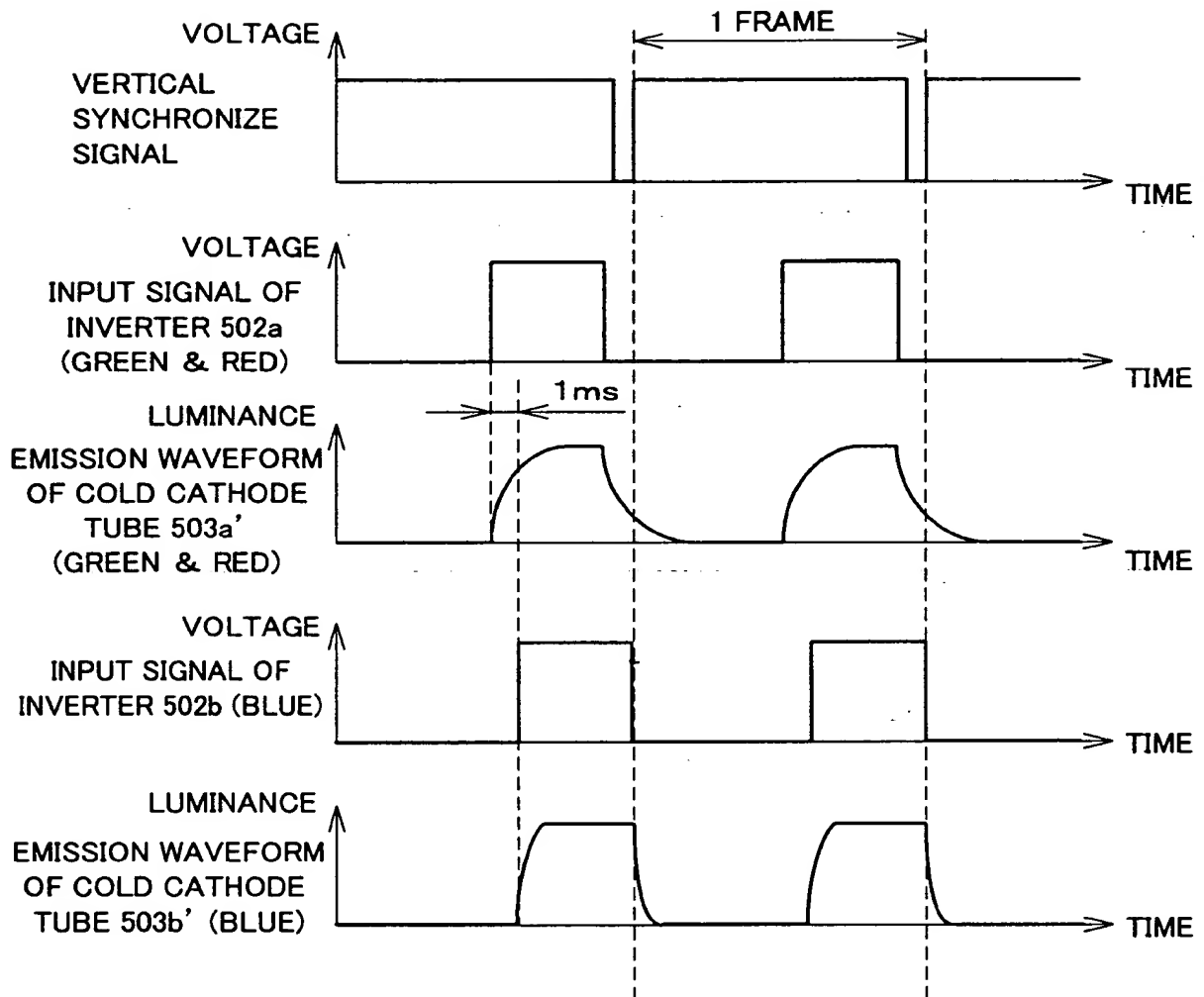
[illegible]

FIG. 19

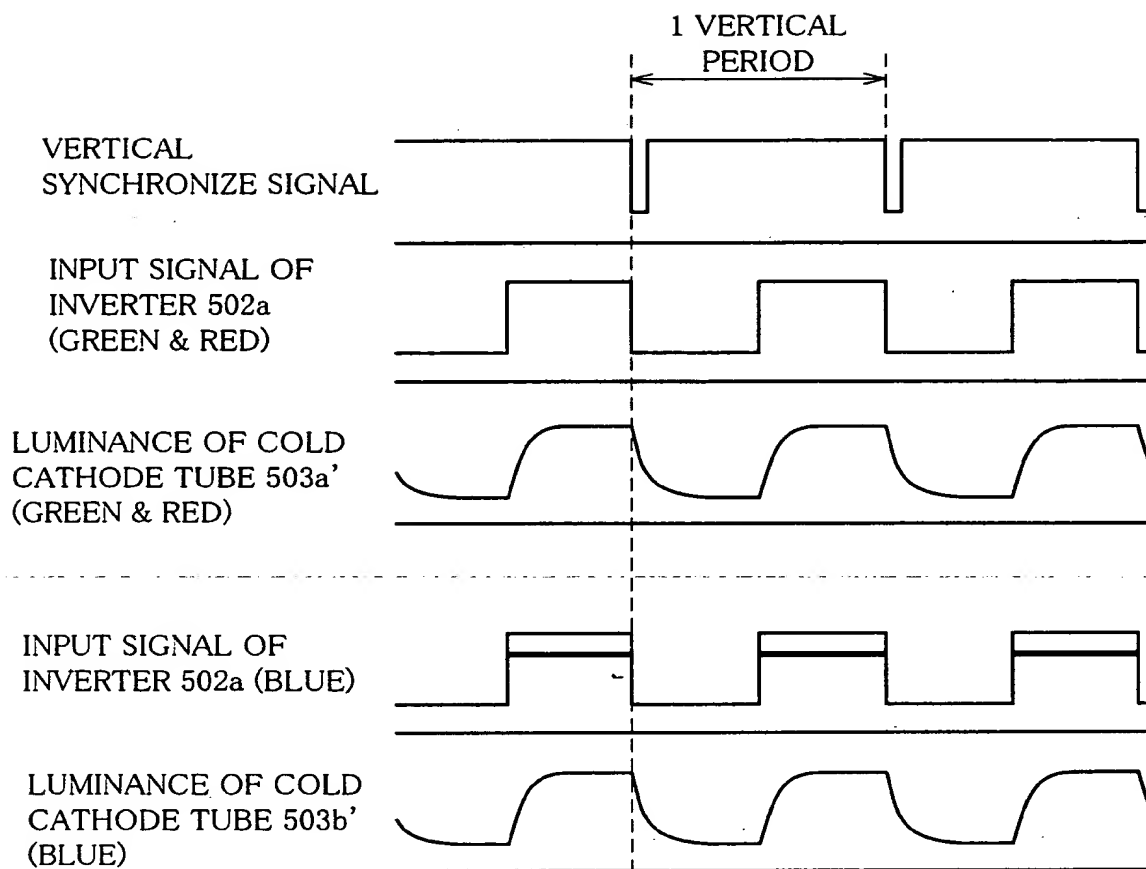


FIG. 20

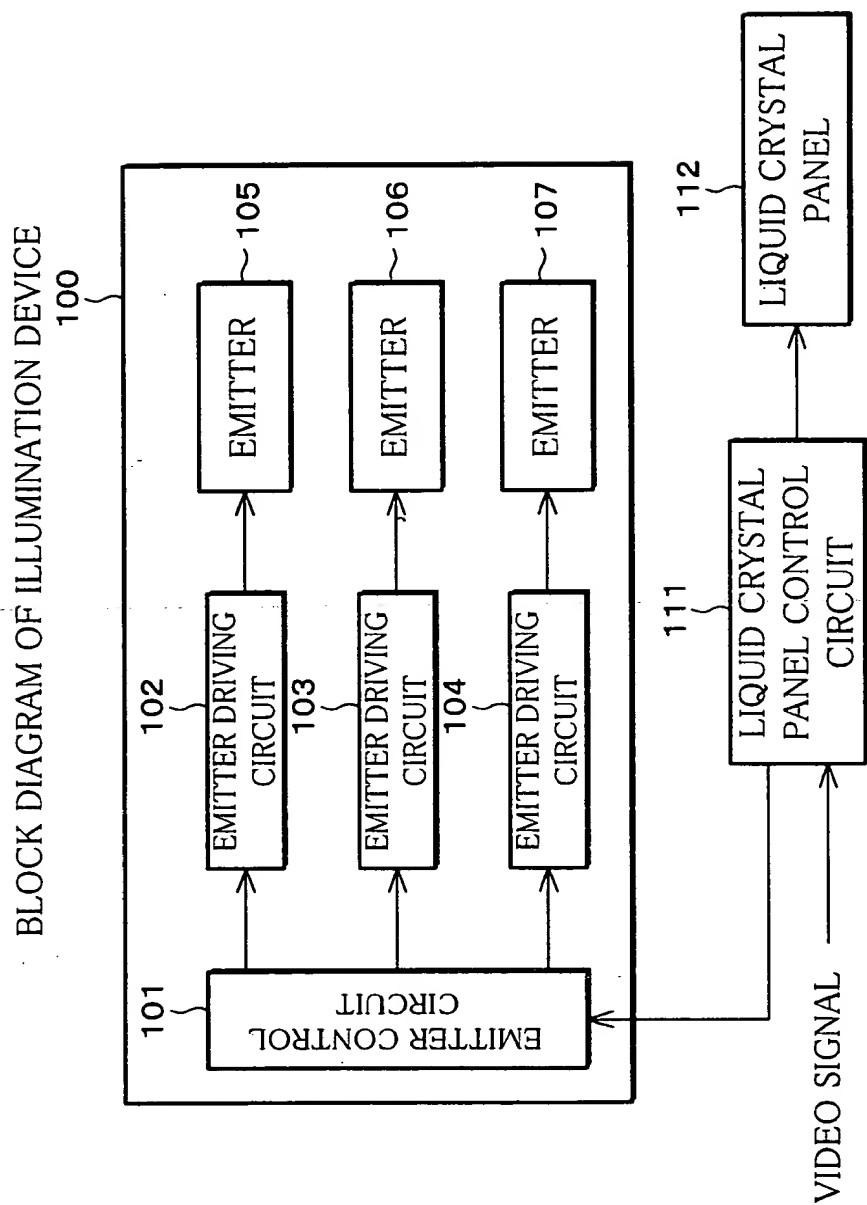


FIG. 21

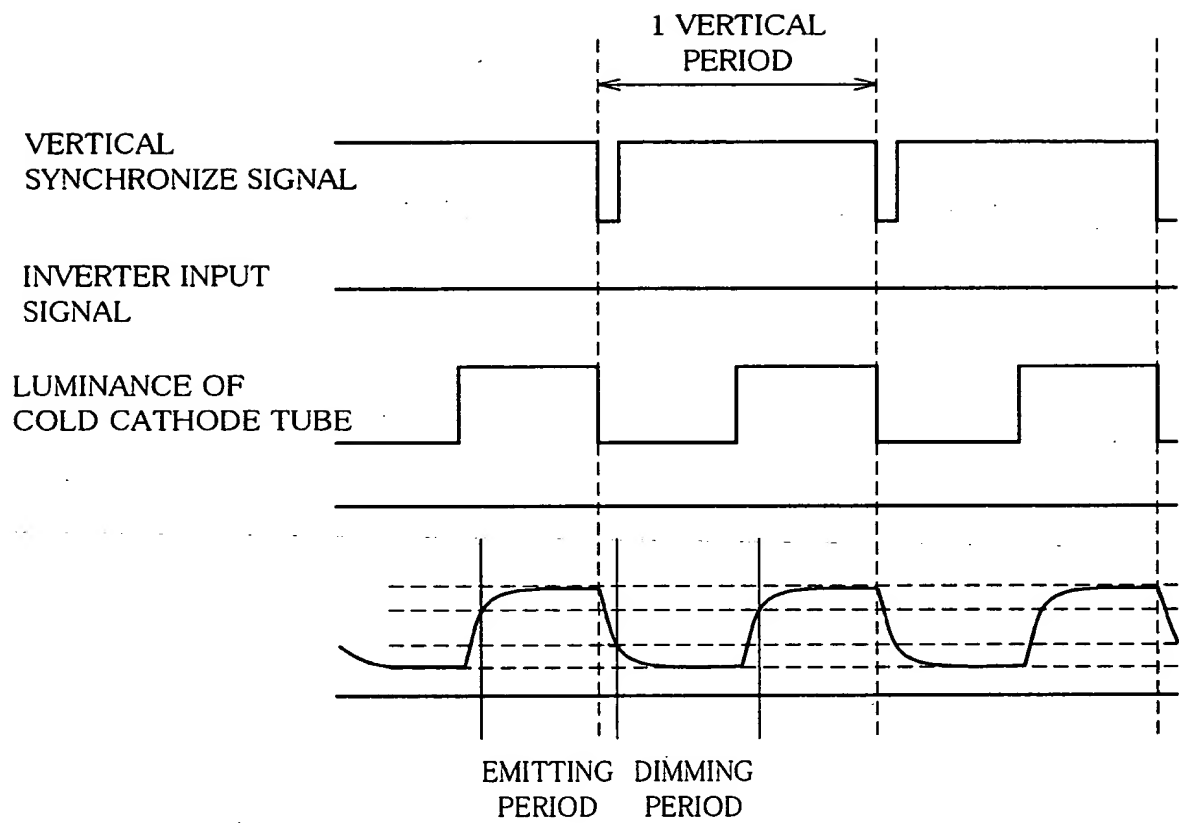


FIG. 22 (a)

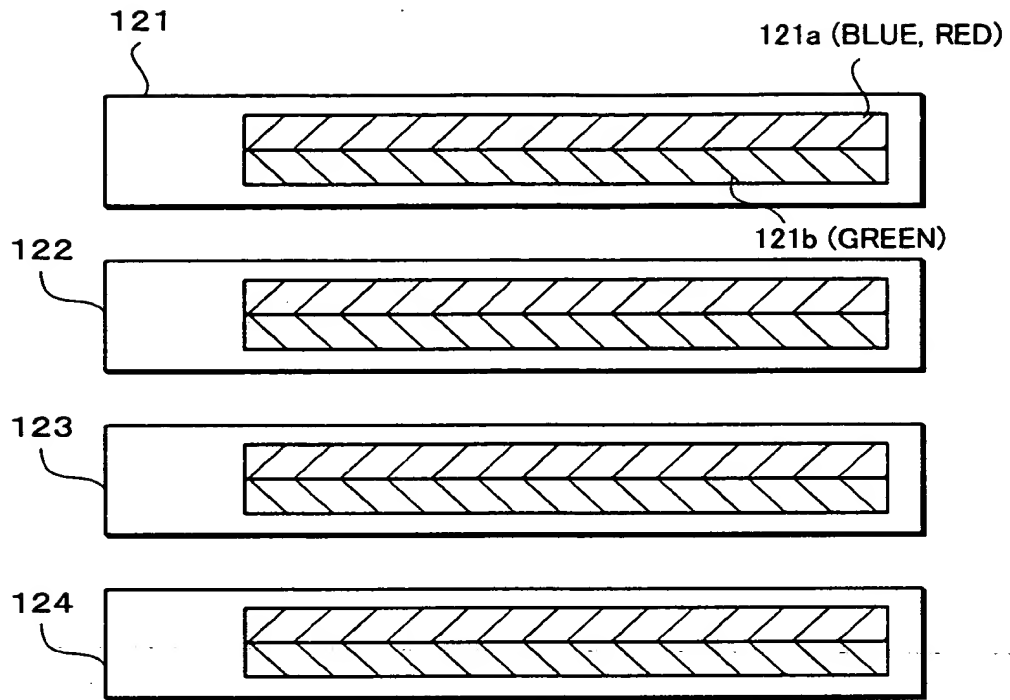


FIG. 22 (b)

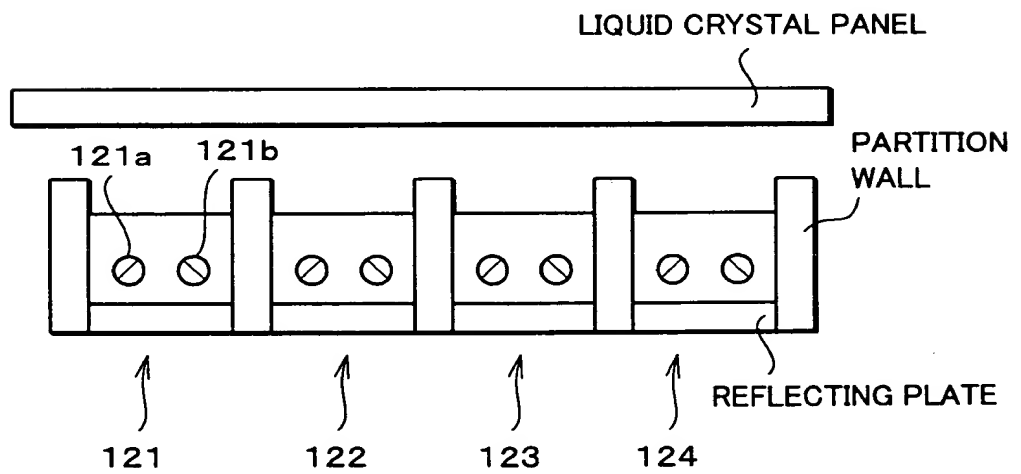
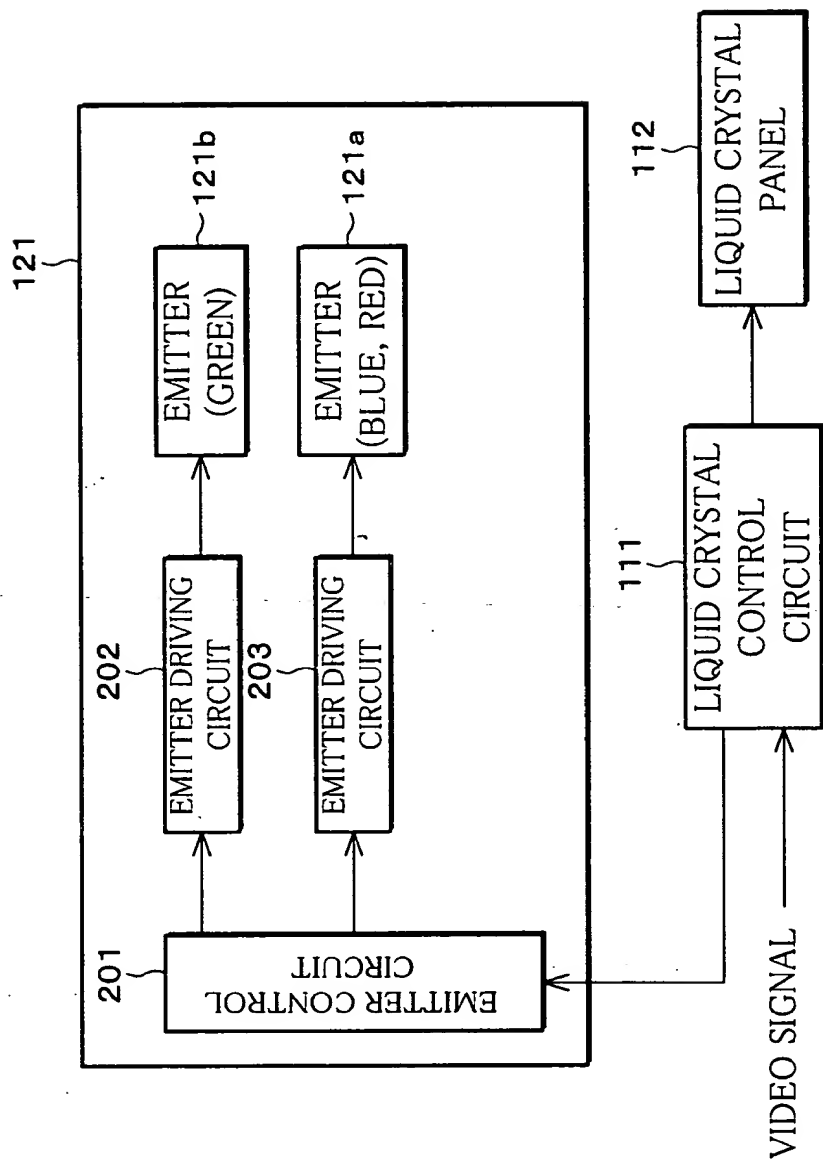


FIG. 23



THE

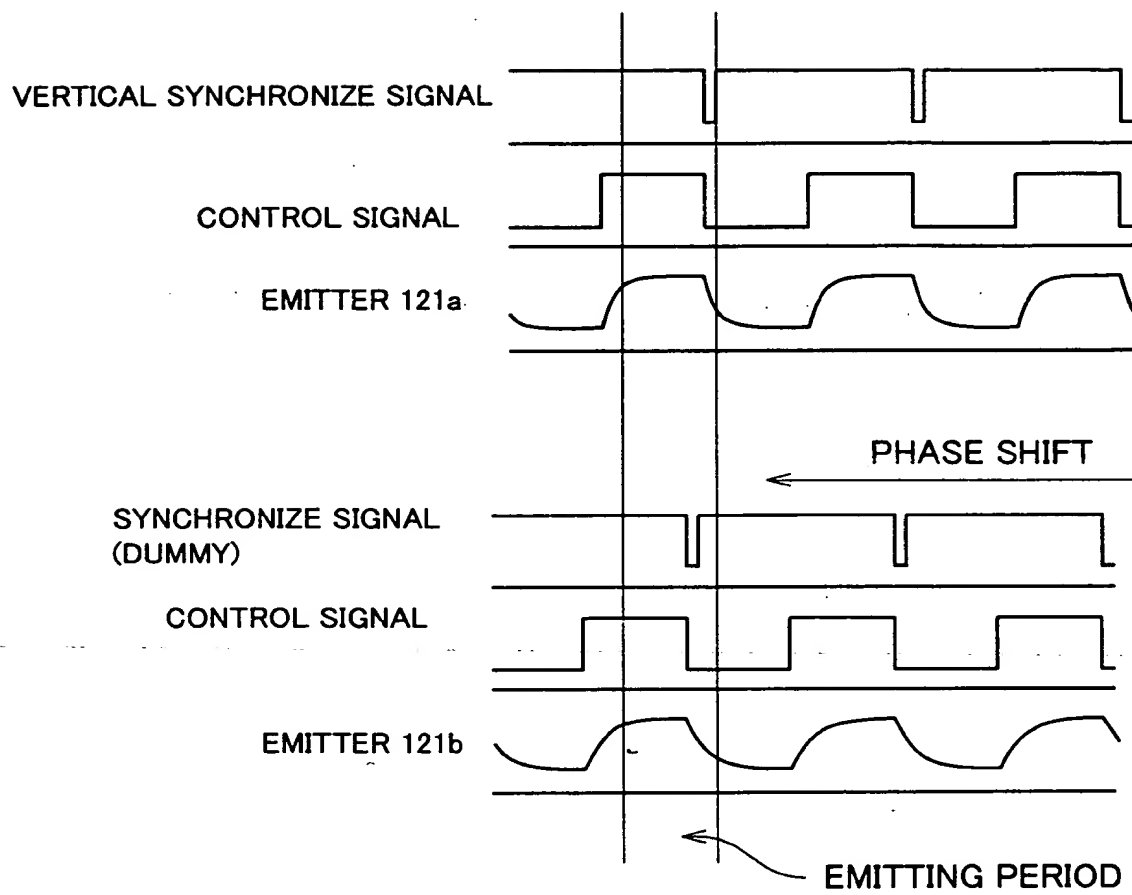


FIG. 25

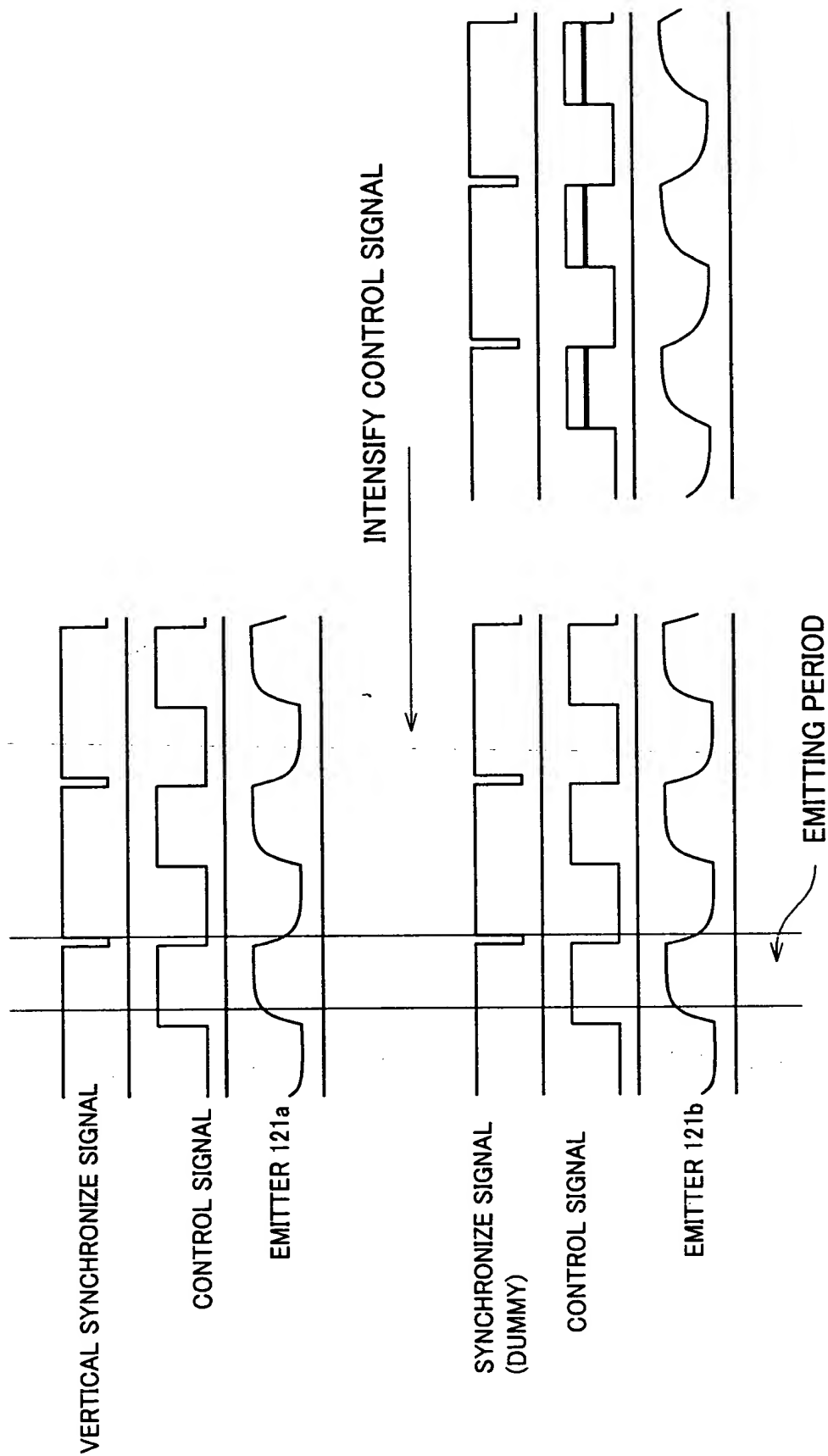


FIG. 26

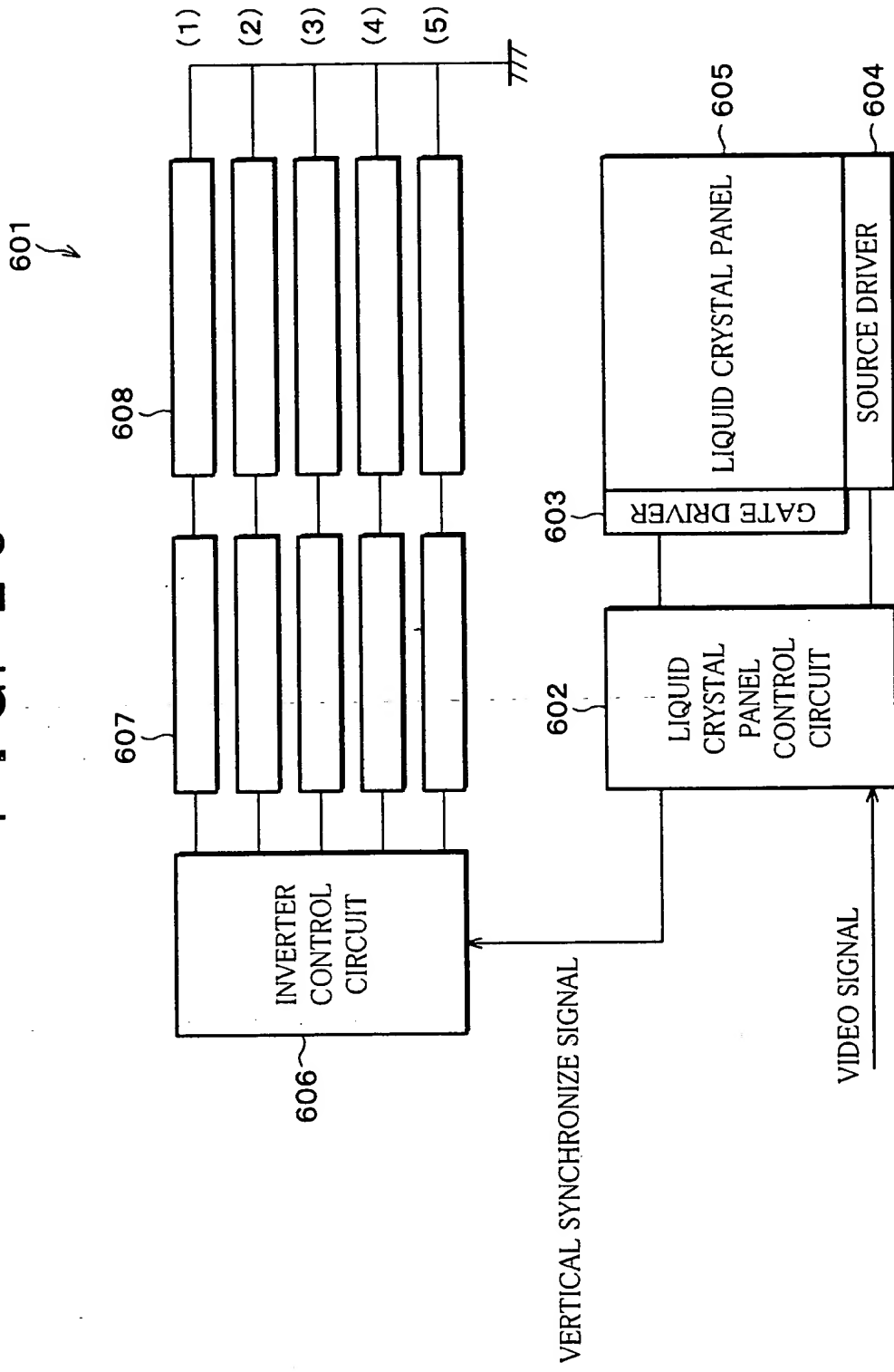


FIG. 27

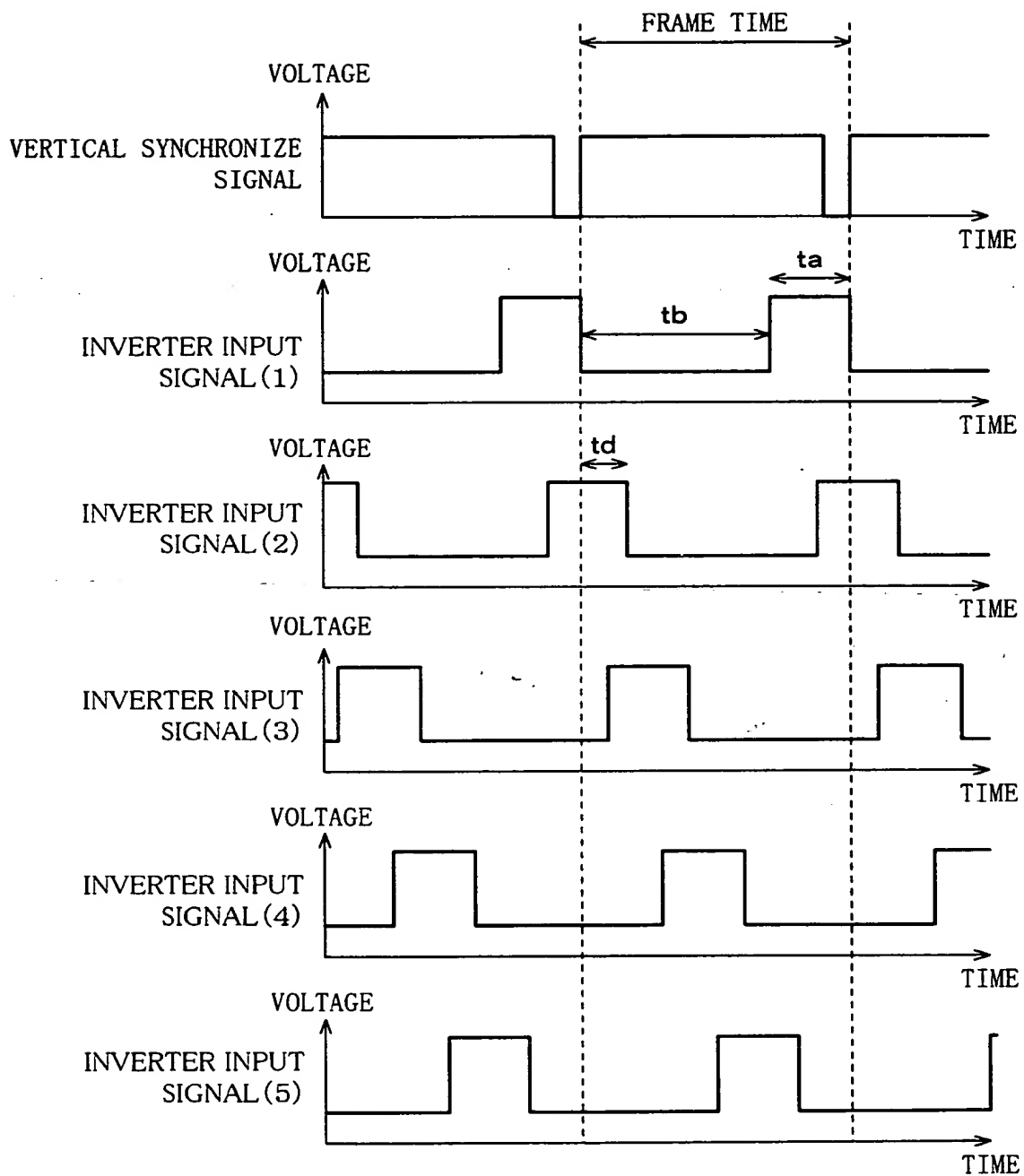


FIG. 28

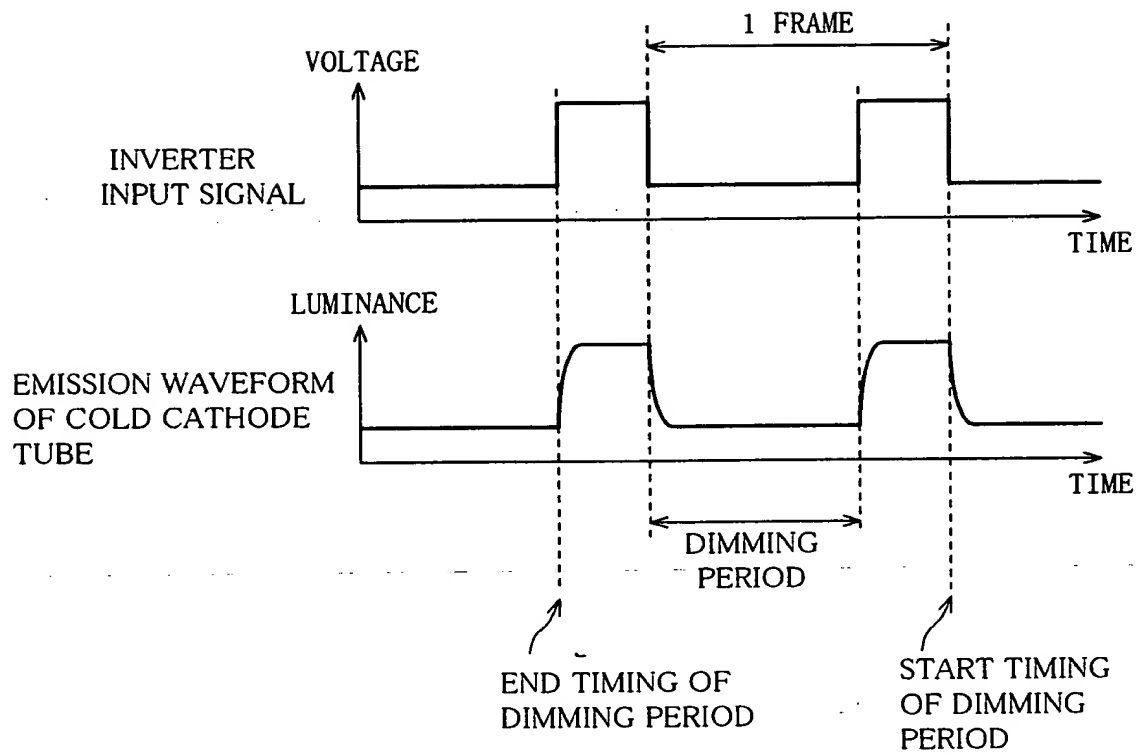


FIG. 29

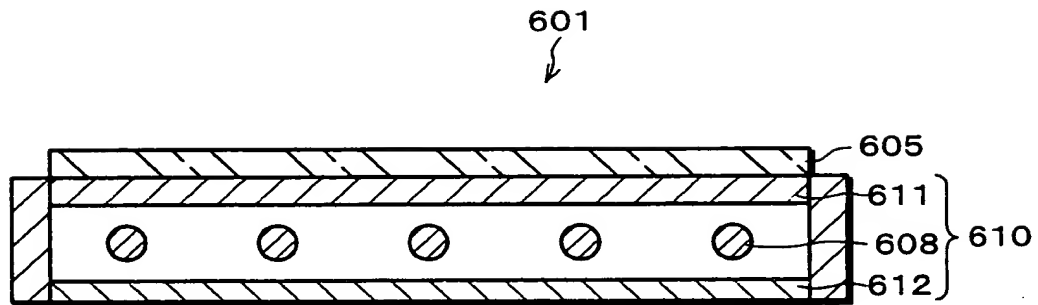


FIG. 30

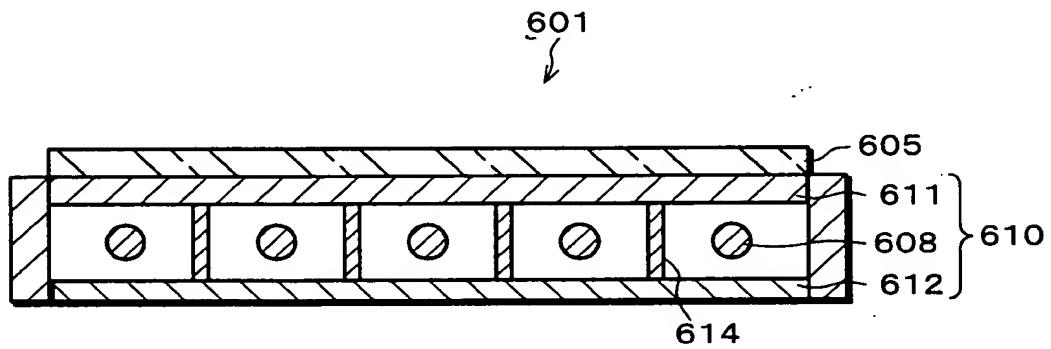


FIG. 31

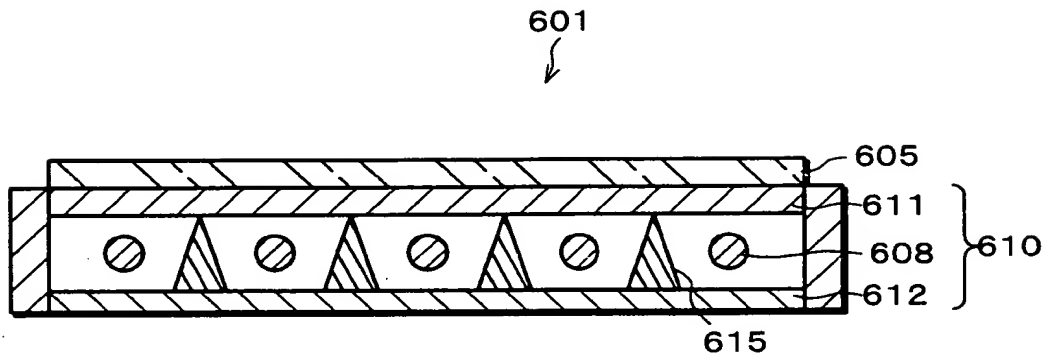


FIG. 32

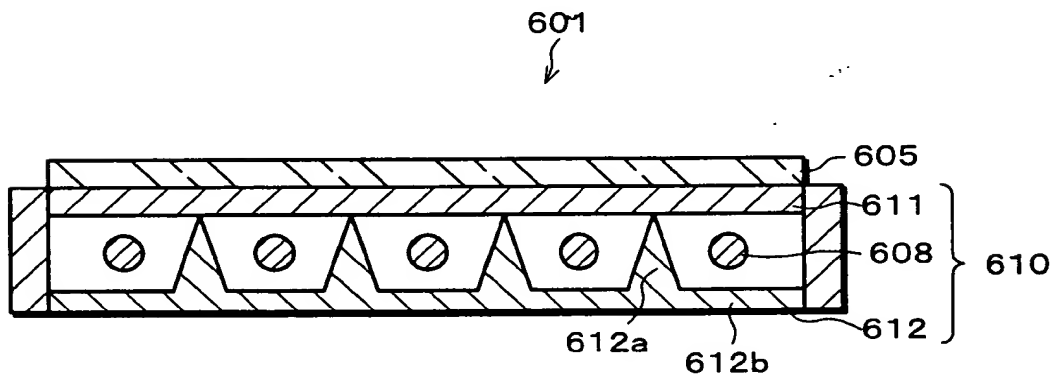


FIG. 33

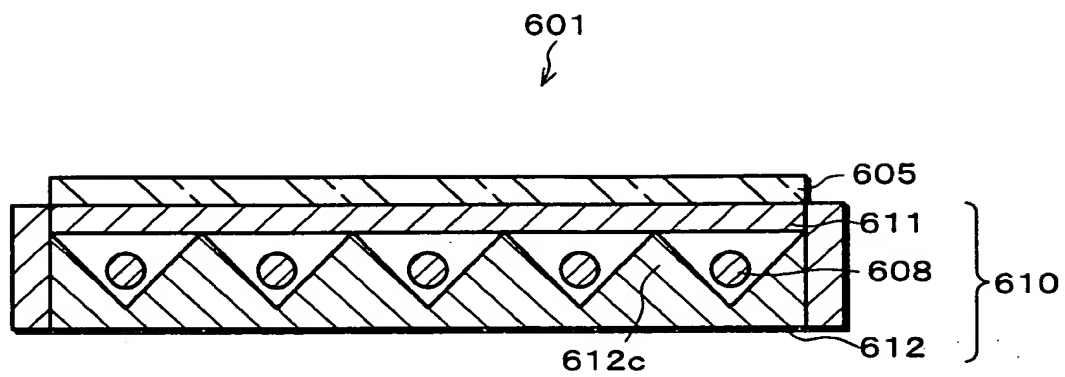
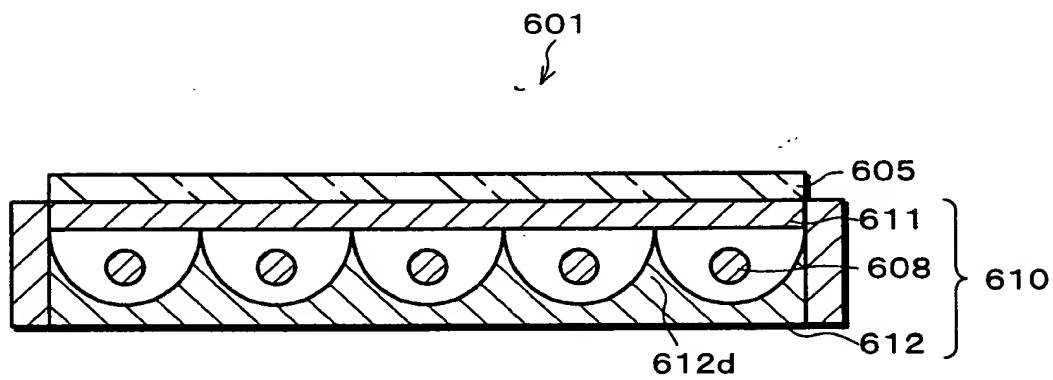


FIG. 34



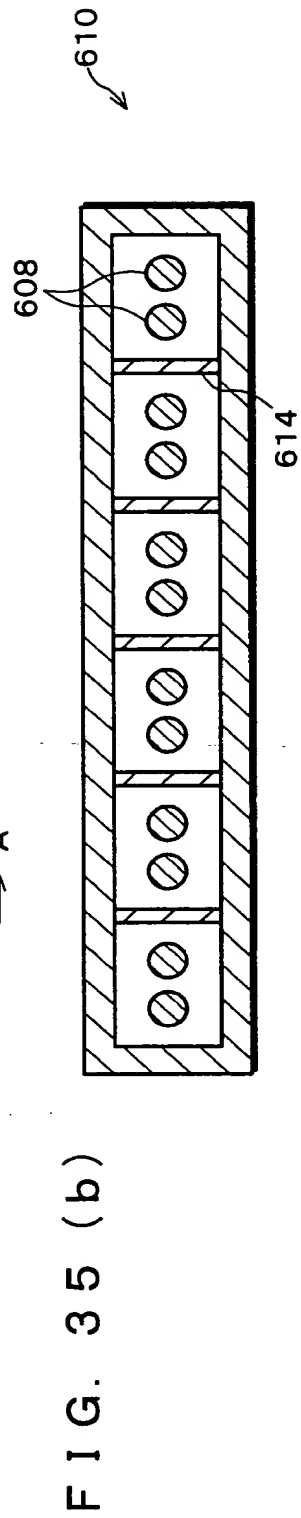
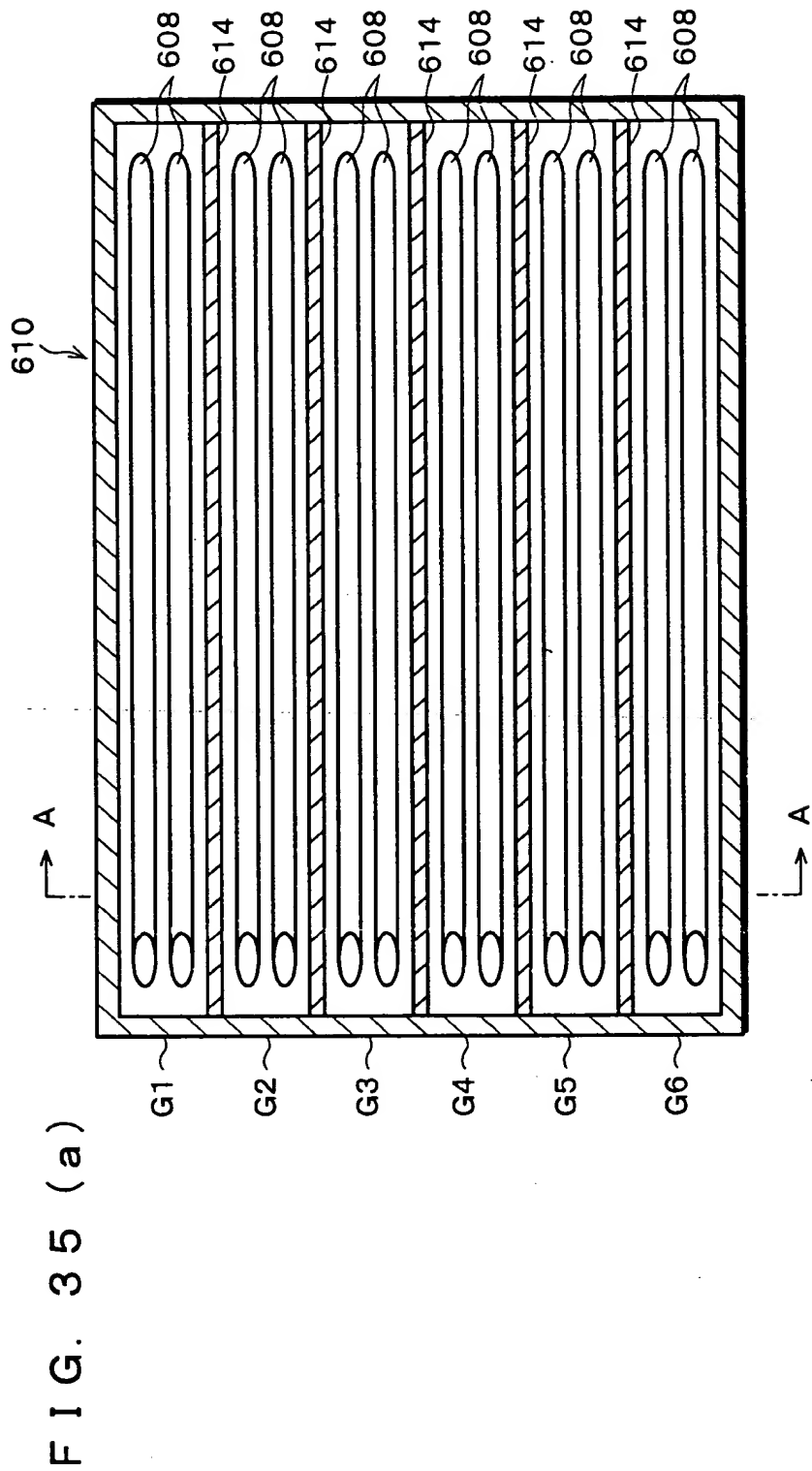


FIG. 36

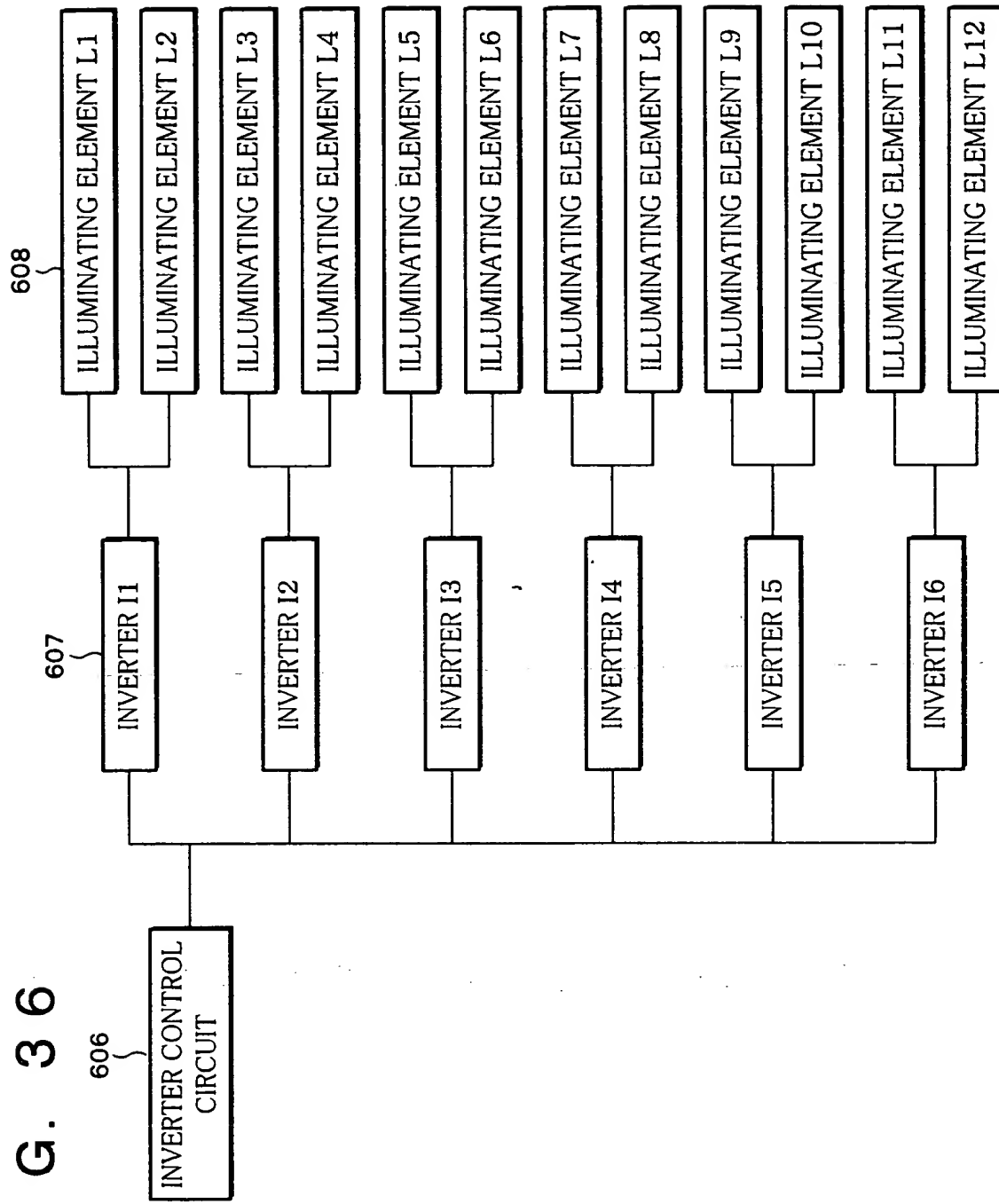


FIG. 37

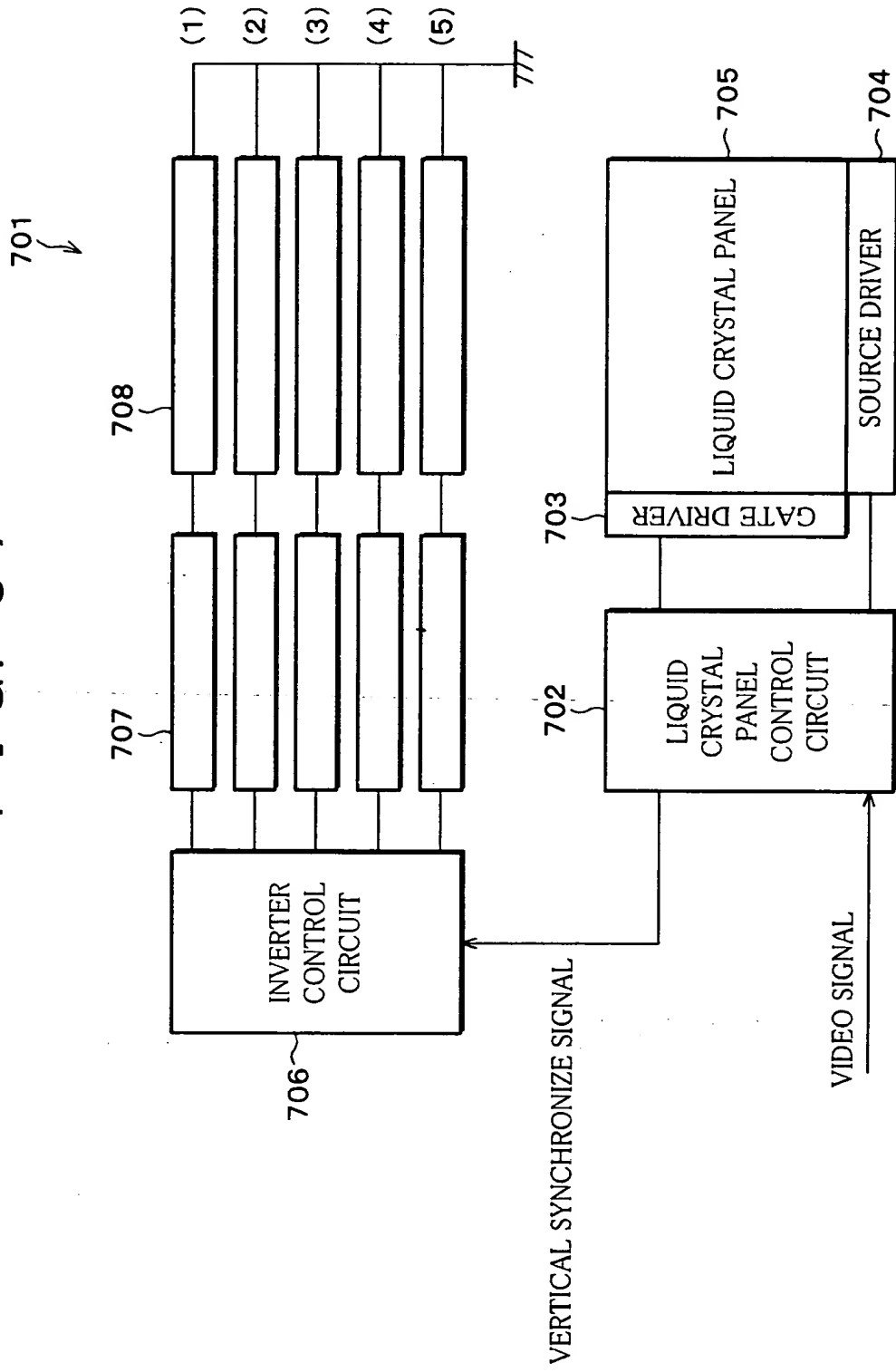
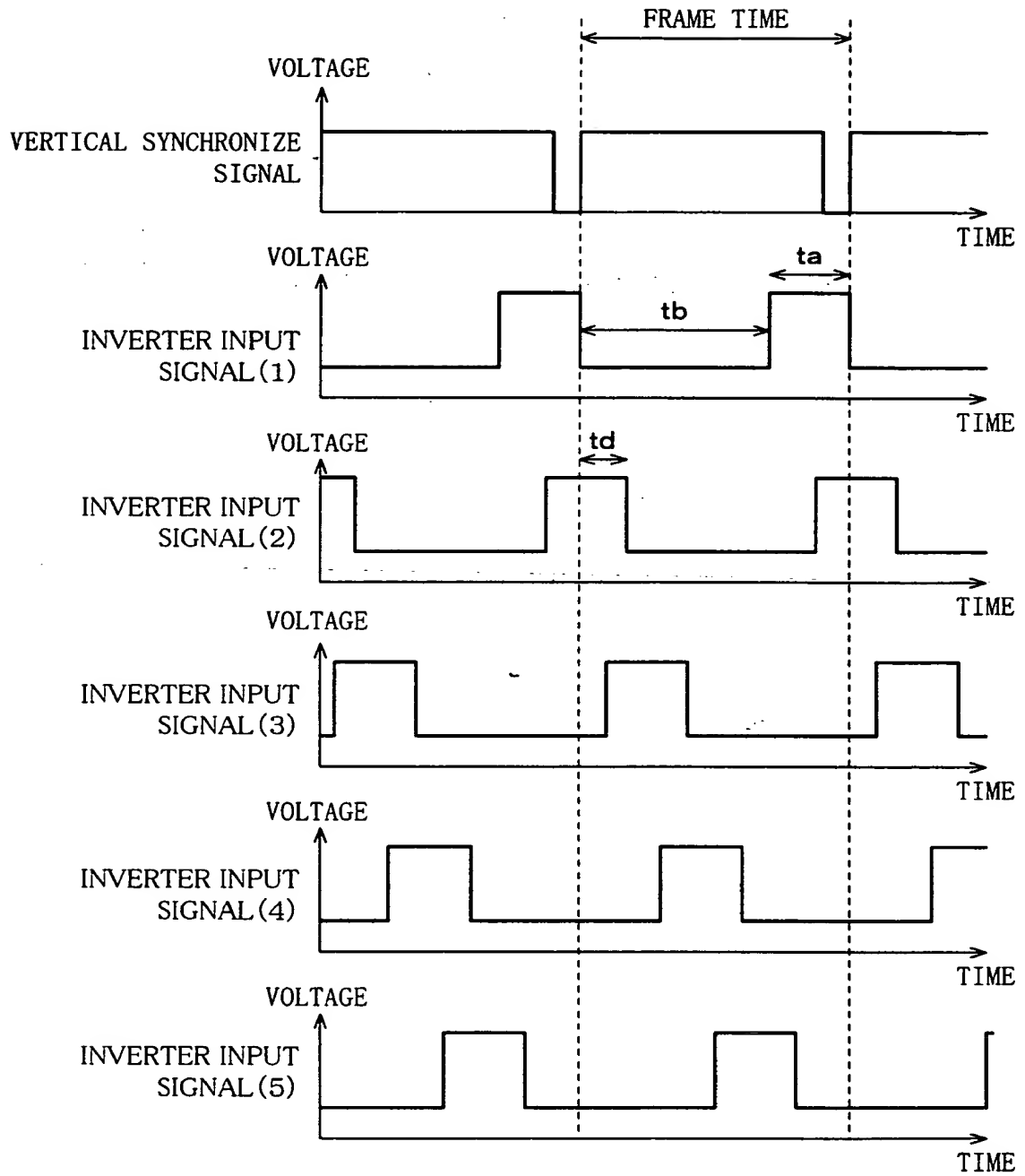


FIG. 38



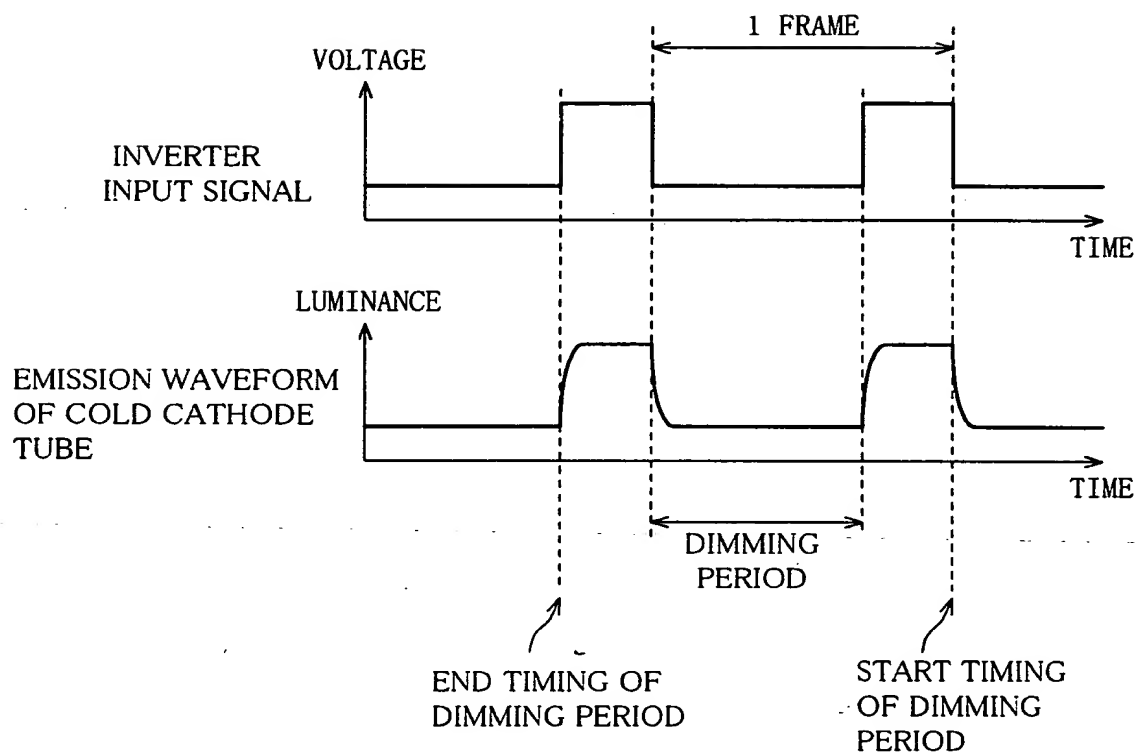
[illegible]

FIG. 40

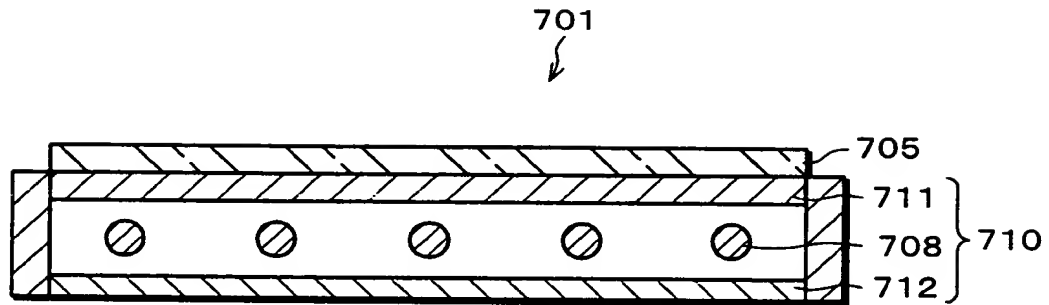


FIG. 30" 63284550

FIG. 41

END TIMING OF DIMMING PERIOD

9/10	⊙	○	△	△	△	△	△	△	△	×
8/10	⊙	○	△	△	△	△	△	△	×	○
7/10	⊙	○	△	△	△	△	△	×	○	○
6/10	⊙	○	△	△	△	△	×	○	○	○
5/10	⊙	○	△	△	△	×	○	○	○	○
4/10	○	○	△	△	×	○	○	○	○	○
3/10	○	○	△	×	○	○	○	○	○	○
2/10	○	○	×	○	○	○	○	○	○	○
1/10	○	×	○	○	○	○	○	○	○	○
0/10	×	○	△	△	△	△	△	△	△	△
f	0/10	1/10	2/10	3/10	4/10	5/10	6/10	7/10	8/10	9/10

START TIMING OF DIMMING PERIOD

FIG. 41

FIG. 42

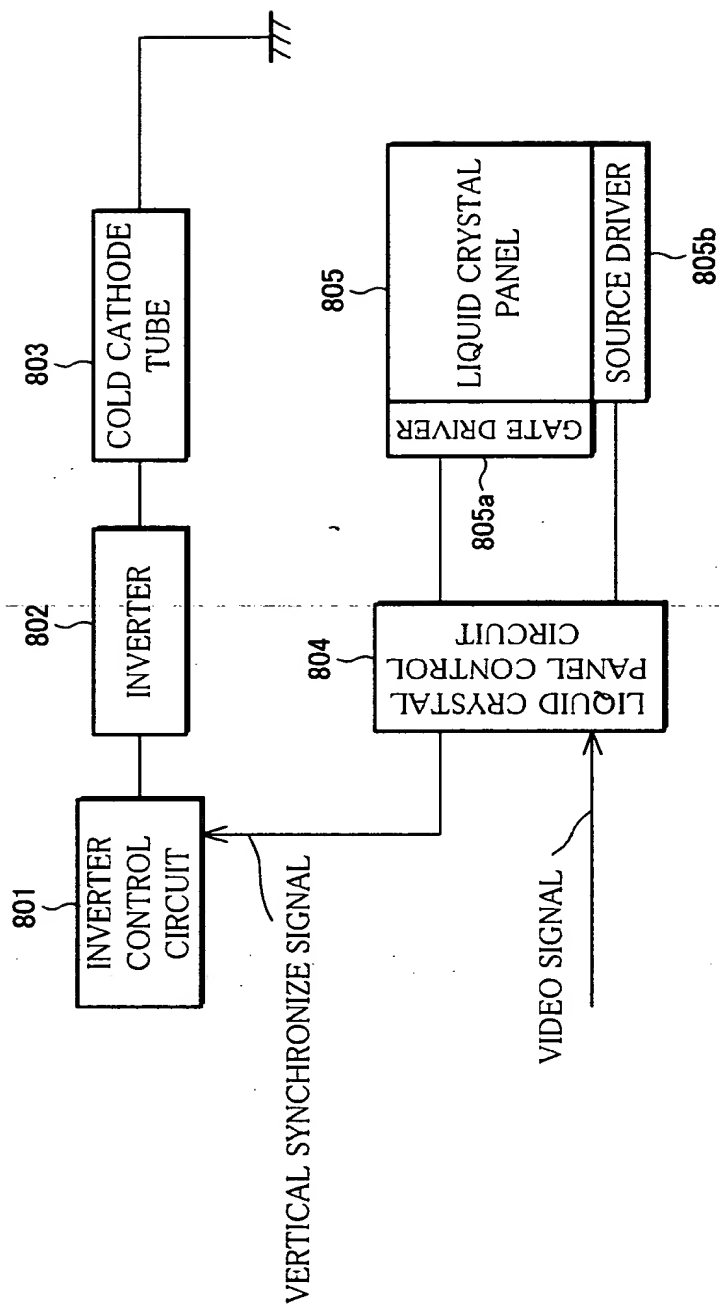


FIG. 43

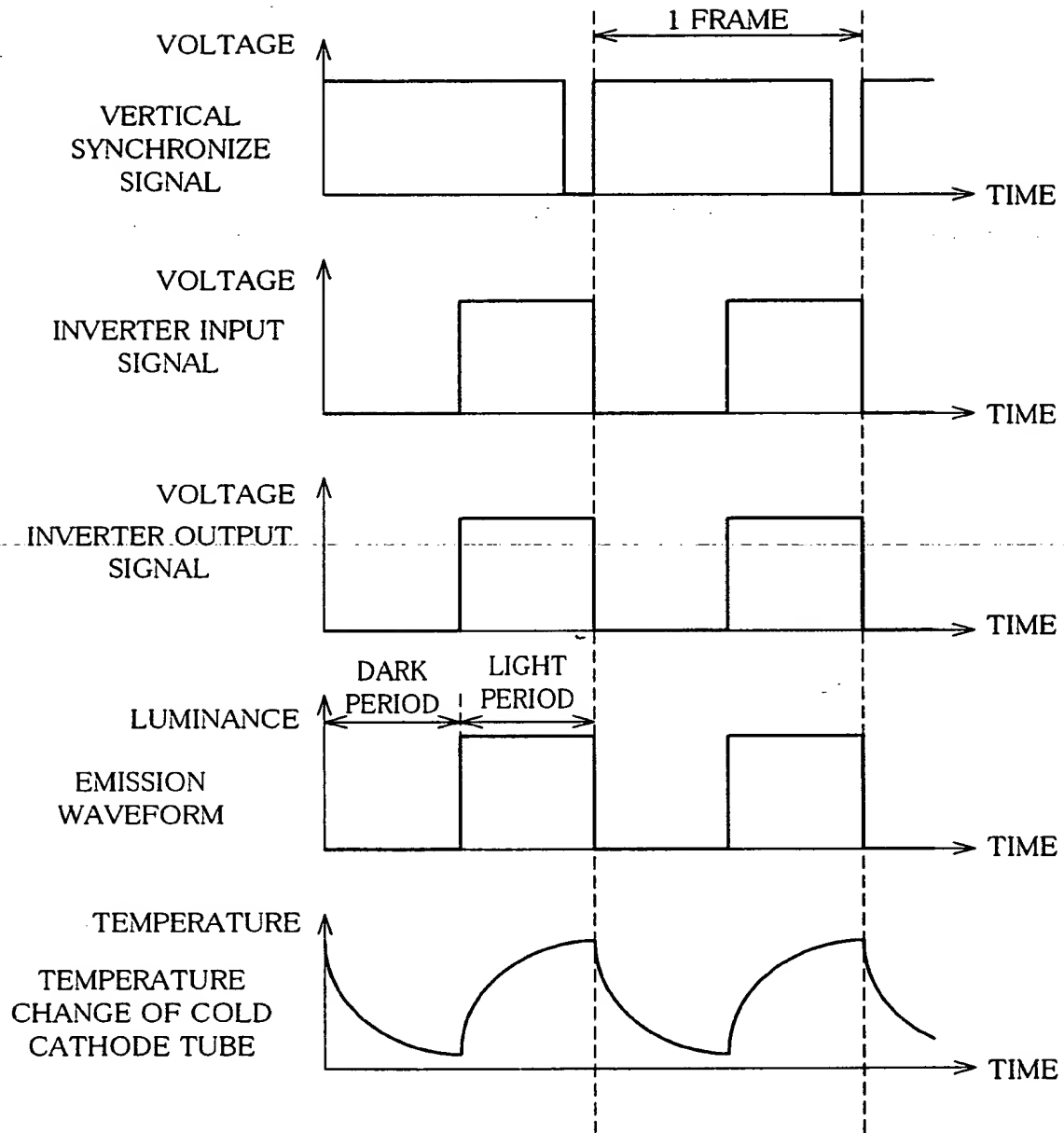


FIG. 43

FIG. 44

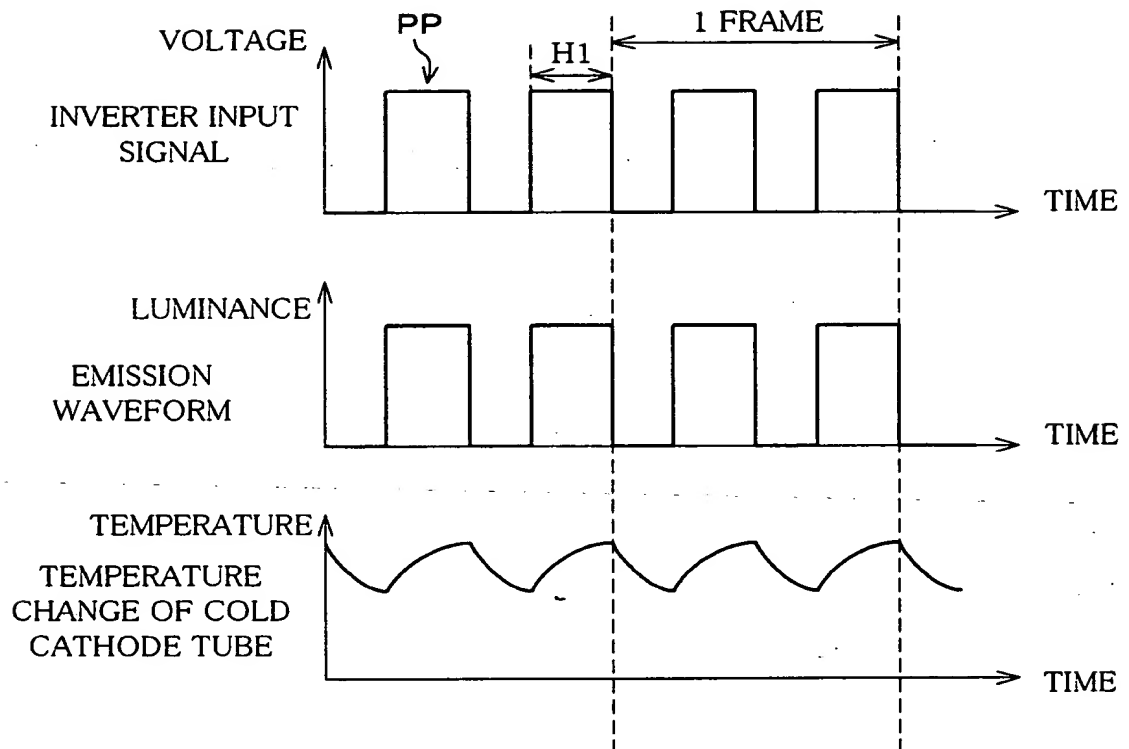


FIG. 44

FIG. 45

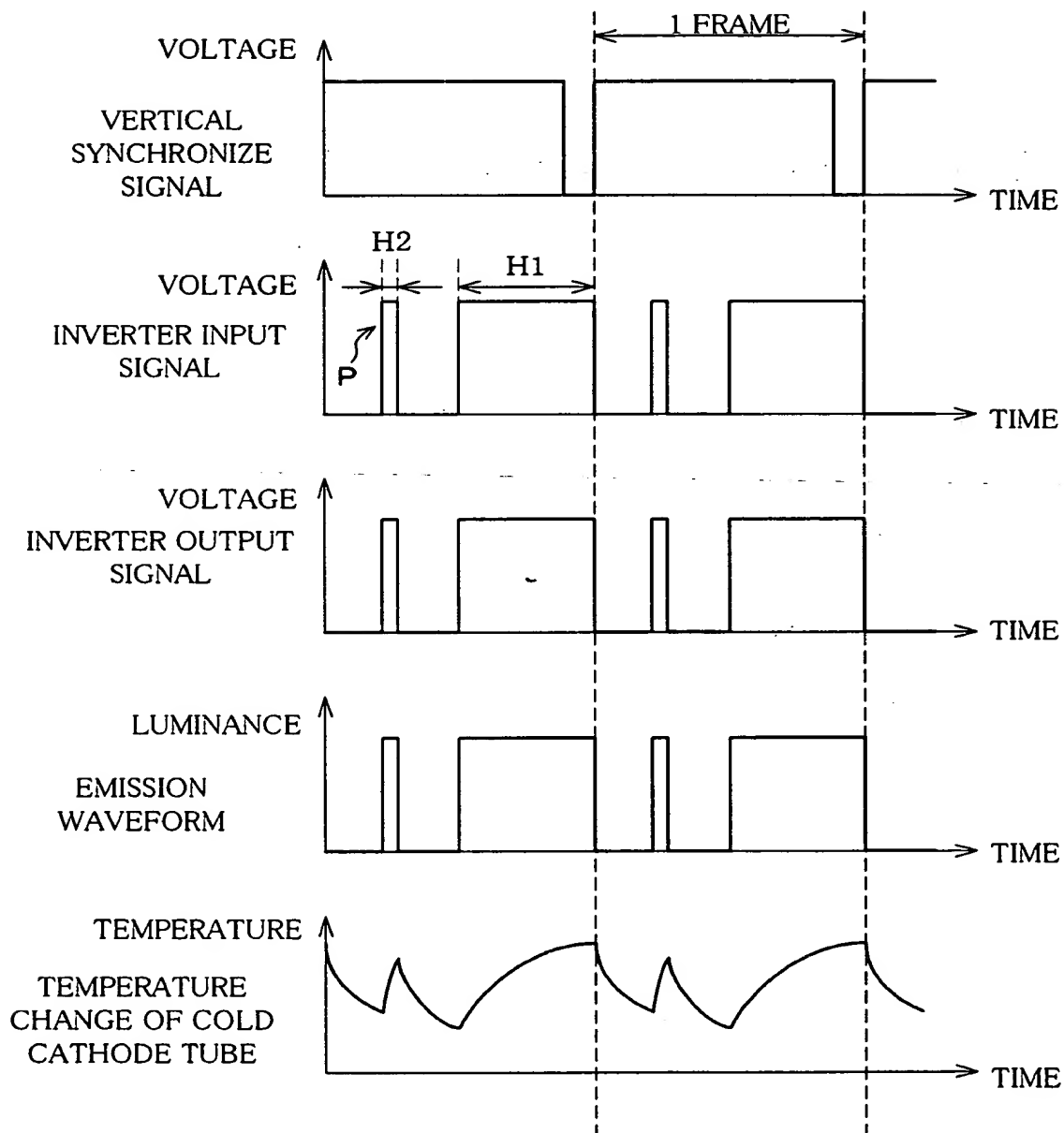
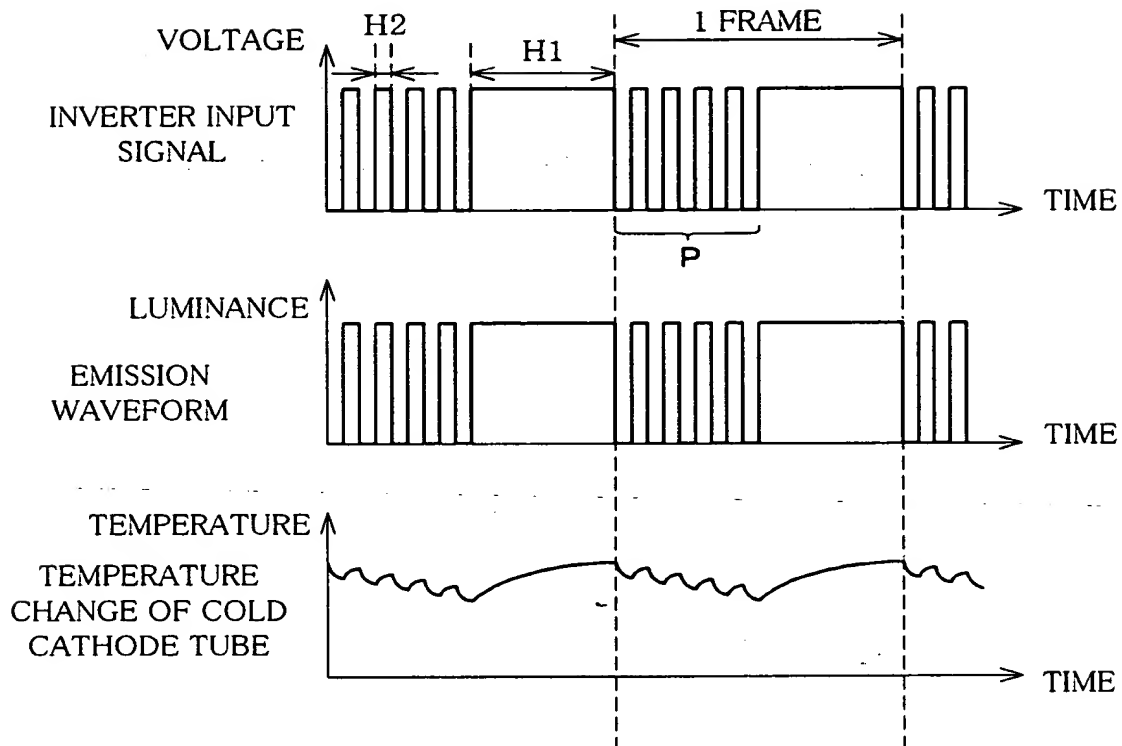


FIG. 45

FIG. 46



100-232-0000

FIG. 47

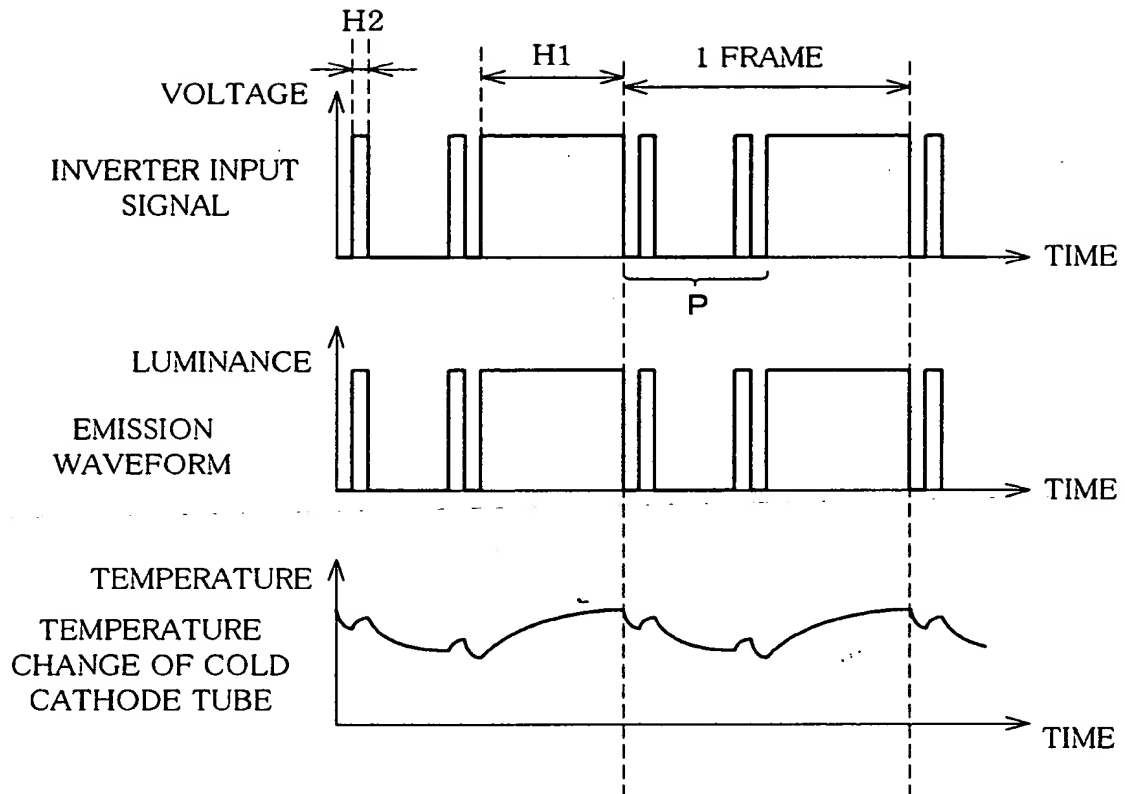


FIG. 48

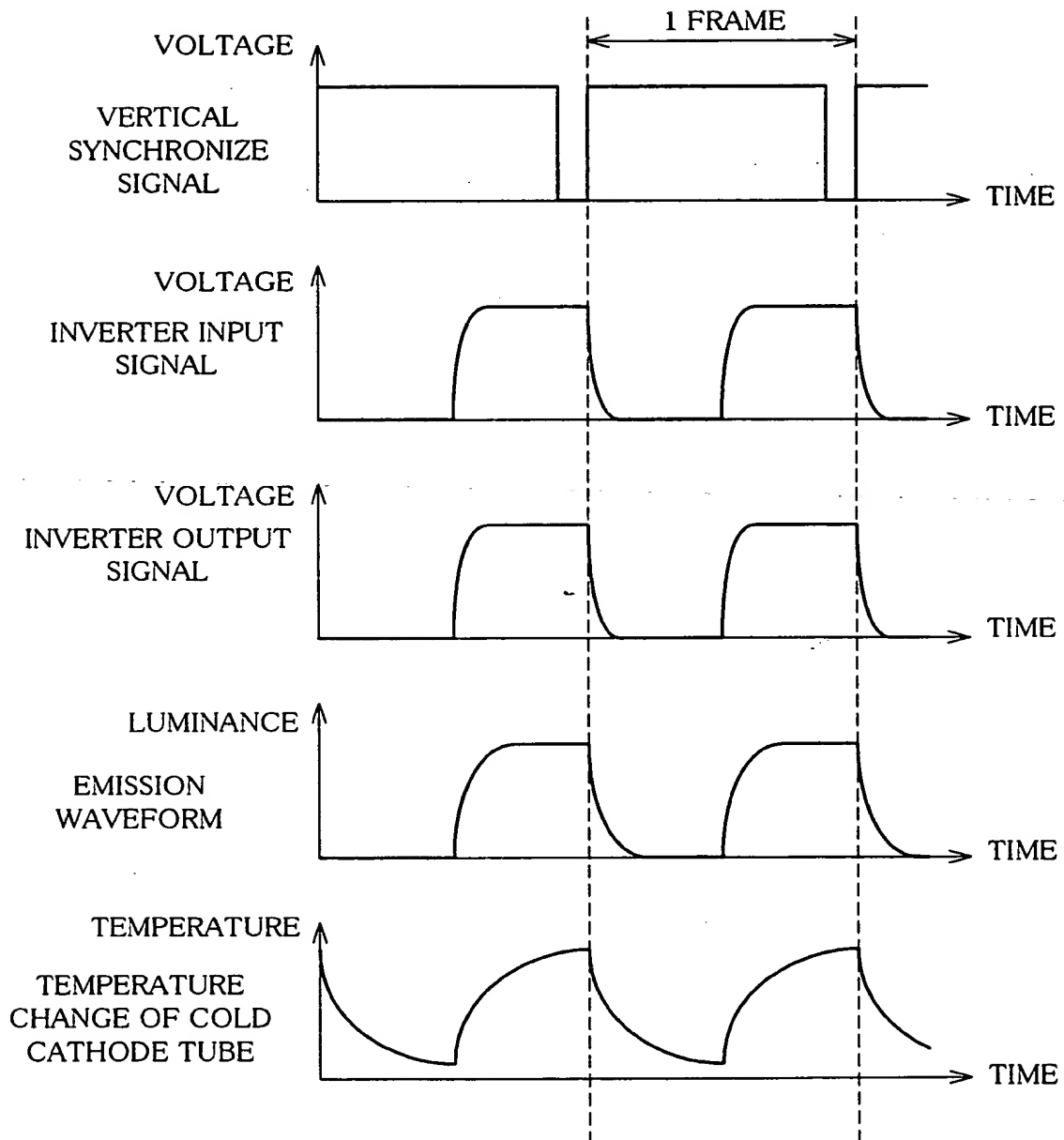


FIG. 49

